QUANTA COMPUTER INC.

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DT3

PCI DEVICES	IDSEL #	REQ/GNT #	IRQ
AGP	N/A	N/A	A, C
LAN	AD16	0	B
Card bus	AD17	1	C
1394	AD18	2	D
MINI-PCI	AD19	3	E,F

LCD Panel	LCD ID2 SB PANEL_ID2	LCD ID1 SB PANEL_ID1	LCD ID0 SB PANEL_ID0
17" SXGA	1	1	1
15" XGA	0	1	1

LVDS Panel
LVDS
AGP Board
Graphic Chip
ATi or nVIDIA
Y/C RGB AGP 8X BUS
AGP Connector
PAGE 13
AGP 8X BUS
Y/C
S-Video
PAGE 20
RGB
CRT
PAGE 14

INTEL P4 CPU
Northwood/ Prescott
(FCPGA 478)
PAGE : 03,04
Host Bus
(400/533/800MHz)
NORTH BRIDGE
Intel 82865G
(GMCH)
(FC-BGA 932)
PAGE : 05,06,07,08
266/333/400MHz
DDR SDRAM
LONG-DIMM
PAGE 15
LONG-DIMM
PAGE 15

HUB I/F
266MB/s,1.5V
SOUTH BRIDGE
Intel 82801EB
(ICH5)
(mBGA 460)
PAGE : 9,10,11

HDD
PAGE 20
PRIMARY IDE
CDROM
PAGE 20
SECONDARY IDE
USB
USB6
PAGE 17
USB7
PAGE 17
USB4
PAGE 17
USB5
PAGE 17
USB2
PAGE 17
USB3
PAGE 17

PCI
GbE LAN MAC+PHY
Broadcom/BCM5788M
PAGE 19
RJ45
PAGE 19
PCI
1394 PHY
TI/TSB43AB22A
PAGE 21
1394
PAGE 21
1394
PAGE 21
LPC
Audio DJ
O2/ OZ263
PAGE 27
Audio DJ Board
Connector
PAGE 27
Audio Codec
Realtek/ALC202
PAGE 24
MIC-IN
PAGE 24
Headphone
PAGE 25
Audio Amp
NS/LM4873MTE
PAGE 25
Speaker
PAGE 25
Audio Amp
NS/LM4871LD
PAGE 25
Sub-woofer
PAGE 25

BIOS
PAGE 26
LED Board
Connector
PAGE 17
Keyboard
Connector
PAGE 26
KEY MATRIX
PS2
EC
NS/PC87591
(176 Pin LQFP)
PAGE 26
LPC
PCI
AC97
USB 0,1
PCI Board
Connector
PAGE 18
Touchpad Board
Connector
PAGE 17
KB/MS
PAGE 17
FAN1
PAGE 14
FAN2
PAGE 14

PCI Board
PCI
Card Bus
ENE/CB1410
PCMCIA
Slot
RJ11
PAGE 19
PCI
Mini-PCI
Slot
USB1/MDC
MDC Slot
USB0
Card Reader
Connector

COM
PAGE 22
LPT
PAGE 20
Super IO
NS/PC87391
(100 Pins
TQFP)
PAGE 23
FDD
PAGE 23

3*CPU/CPU-
1*SDRAM
5*3V66
3*PCIF
7*PCI
2*REF
2*48MHz
1*SRC/SRC#
Main
CLOCK
GEN.
ICS952623
PAGE : 12

Power
VCCRTC
RTC
PAGE : 22
VIN
VA2
VAD
BATTERY & ACIN
PAGE : 28
5V_HDD
12V_HDD
12V_HDD & 5V_HDD
PAGE : 34
VCC2.5_M
2.5VSUS
DDR_VTT
+2.5V
2.5VSUS&DDR_VTT
PAGE : 32
5VPCU
3V_591
5VSUS
3VSUS
+5V
3V-S5
+3V
SYSTEM POWER
PAGE : 30
+1.5V
POWER DISCHARGE
PAGE : 32
VCC_CORE
POWER CORE
PAGE : 33
VCCVID
VTT_NB
POWER VTT_NB
PAGE : 29

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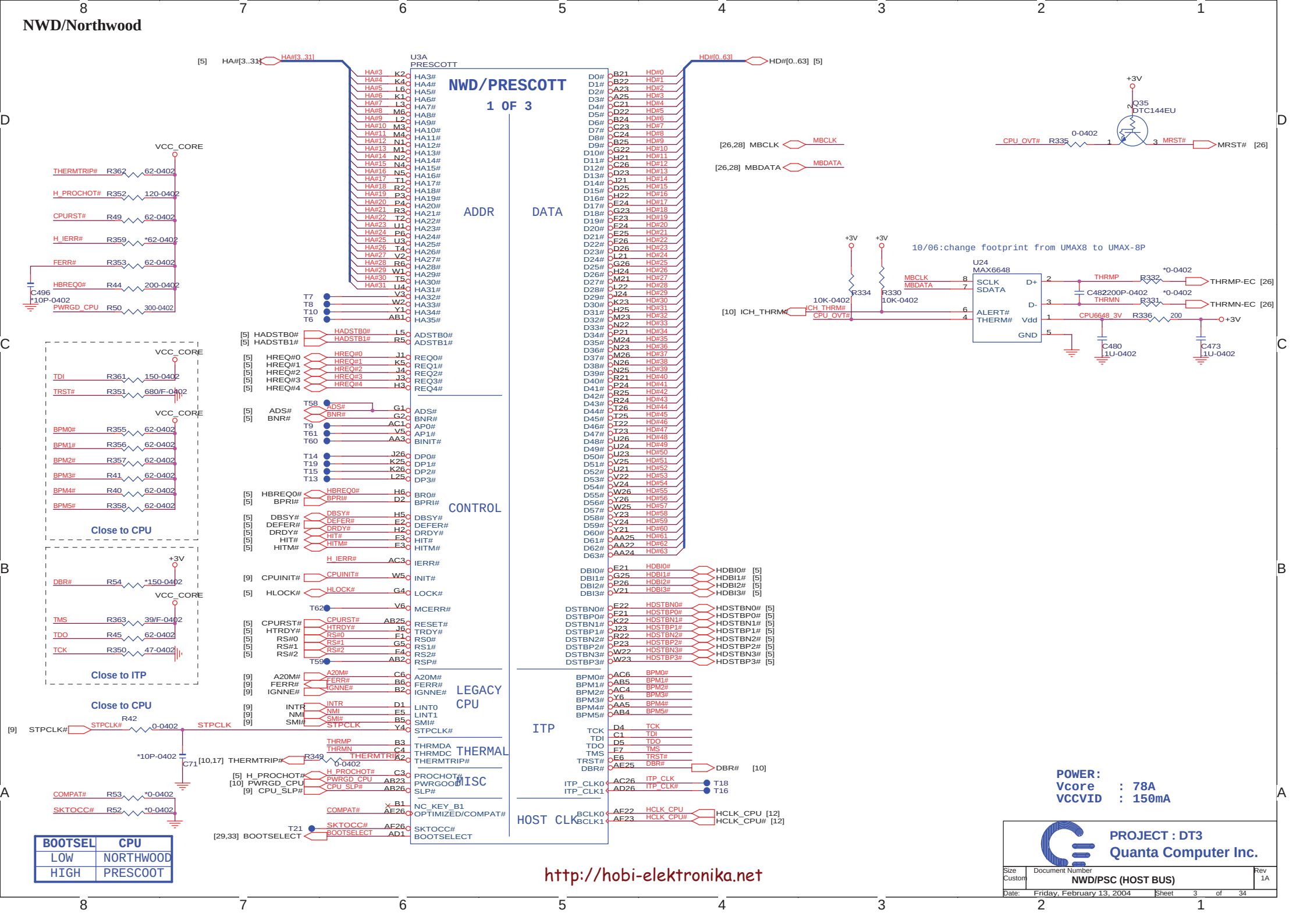

BOOTSEL	CPU
LOW	NORTHWOOD
HIGH	PRESCOOT

POWER:
Vcore : 78A
VCCVID : 150mA

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Size: Custom	Document Number: NWD/PSC (HOST BUS)	Rev: 1A
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PROJECT : DT3
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U3B PRESCOTT — NWD/PRESCOTT 2 OF 3 — MISC POWER

U3C PRESCOTT — NWD/PRESCOTT 3 OF 3 — GROUND

VCCVID
R24 2.7K-0402
VCCVIDOK

[33] VID0
[33] VID1
[33] VID2
[33] VID3
[33] VID4
[33] VID5
w/s->5/5

[33] VCOER_SENSE
[33] VSS_SENSE
150mA
25 mils(min)
C481 .1U-0402
[12,33] VCCVIDOK

+3V
VID0 R347 1K-0402
VID1 R346 1K-0402
VID2 R343 1K-0402
VID3 R344 1K-0402
VID4 R345 1K-0402
VID5 R348 *1K-0402

VCC_CORE
L11 10UH-0805
L10 10UH-0805
60mA
12mils
H_VCCIOPLL
H_VCCA
H_VSSA
C95 33U/10V-3528
C565 330U-0402
C93 33U/10V-3528
C559 330U-0402

C89 C88 C35 C37 C544 C543 C91 C90 C545 .1U-0402
C556 C139 C103 C138 C106 C504 C123 C97 .47U/10V
C121 C136 C132 C124 C117 C501 C38 C562 .47U/10V
C520 C78 C521 C539 C49 C98 C86 C503 C84 C57 10U/6.3V-0805
C509 C513 C58 C66 C74 C68 C81 C540 C75 C73 10U/6.3V-0805
C87 C530 C82 C528 C522 C80 C506 C78 C79 C67 10U/6.3V-0805
C72 C83 C539 C538 10U/6.3V-0805
C85 C77 C92 C532 C512 *470U/2.5V-7343

[5] HD_VREF
R37 0-0402
R39 200/F-0402
R38 169/F
C70 .1U-0402
C69 220P-0402
W/S->12mils/15mils
H_GTLREF
GTLREF0 GTLREF1 GTLREF2 GTLREF3

R51 61.9/F
R339 61.9/F
H_COMP0
H_COMP1
COMP0 COMP1

R369 R337 R368 R360 R43 R338 R354 R55 62-0402
TESTHI0 TESTHI1 TESTHI2 TESTHI3 TESTHI4 TESTHI5 TESTHI6 TESTHI7 TESTHI8 TESTHI9 TESTHI10 TESTHI11 TESTHI12

RSVD0 RSVD1 RSVD2 RSVD3 RSVD4

[5,12] HBSEL0
[5,12] HBSEL1
R36 0-0402
R34 0-0402
BSEL0 BSEL1

C135 C558 C102 C133 C118 C45 C101 C115 .47U/10V
C508 C36 C500 C125 C110 C112 C104 C109 .47U/10V

10/16:Mount C512,C532
for Vcore transsion

PROJECT : DT3
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Size: Custom | Document Number: NWD/PSC (POWER/NC) | Rev: 2A
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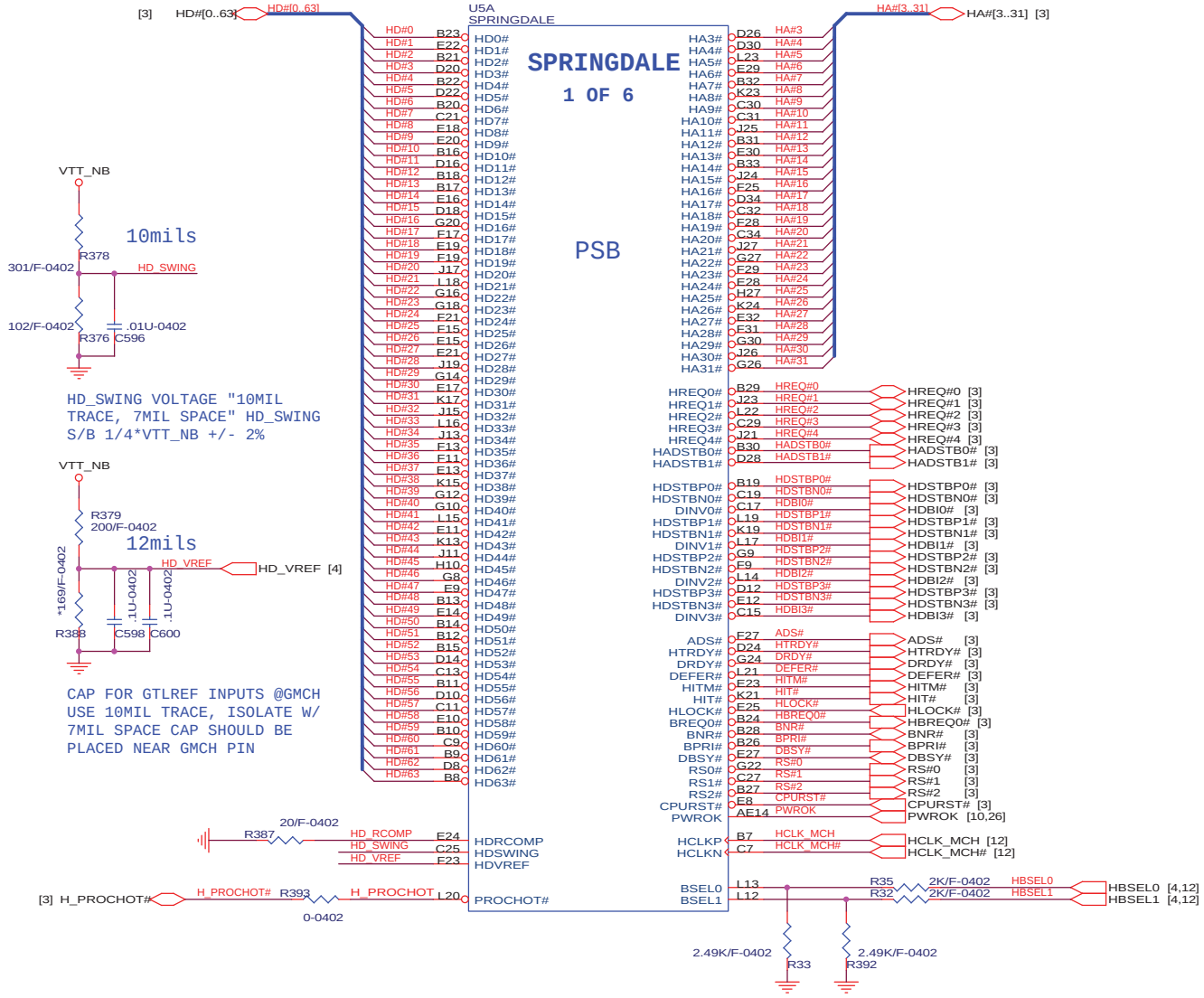
MCH (Host bus)



POWER:

1.5V	: 4.71A
1.225V/1.45V	: 1.6A
2.55V	: 4.9A
3.3V	: 0.2A

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Custom	MCH (Host bus)	1A
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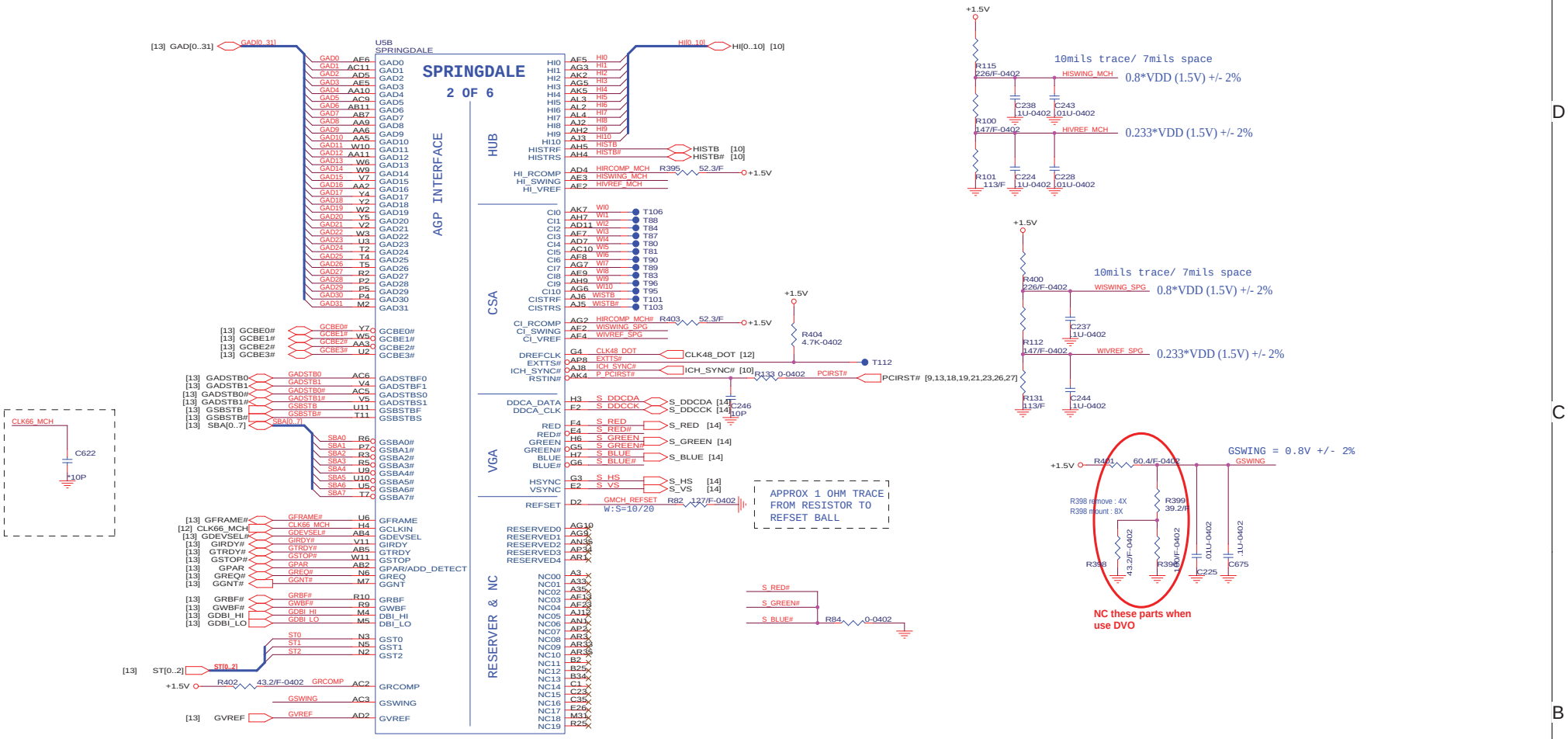
MCH (AGP bus)



PROJECT : DT3
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Size	Document Number	Rev
Custom	MCH (AGP bus & HUB I/F)	1A

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MCH (POWER)

U5F SPRINGDALE

SPRINGDALE
6 OF 6

GROUND

U5E SPRINGDALE

SPRINGDALE
5 OF 6

POWER

09/03:Reverse for test.If VTT_NB can use VCC_CORE,we can remove VTT_NB power ckt

VCC_CORE [3,4,11,32,33]
+1.5V [6,10,11,13,32]
2.5VSUS [8,15,31,32]
+3V [3,4,9,10,11,12,13,14,15,17,18,19,21,23,24,26,27,29,30,31,32,33]
VTT_NB [5,29]

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Size	Document Number	Rev
Custom	MCH (POWER)	1A

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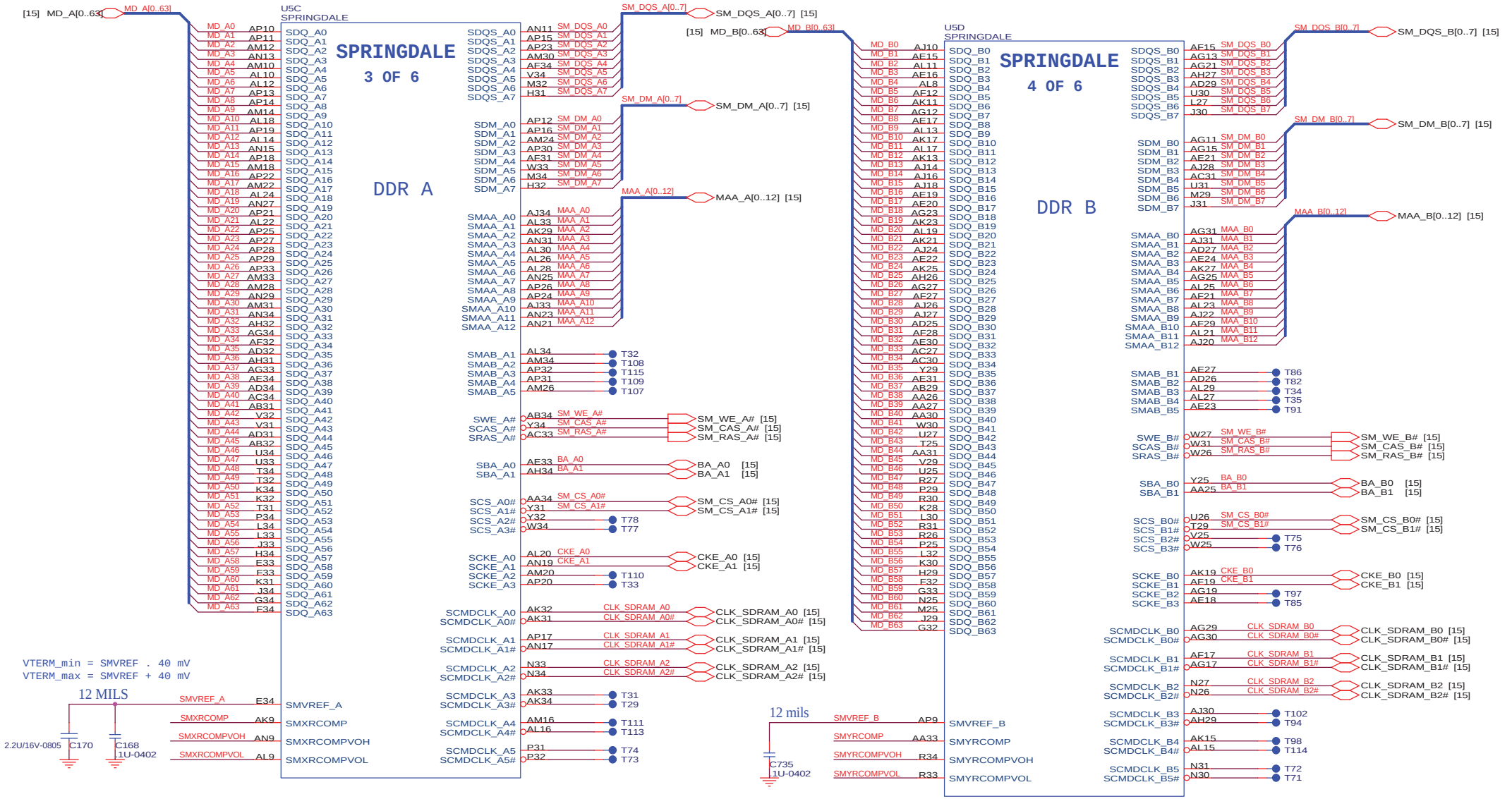
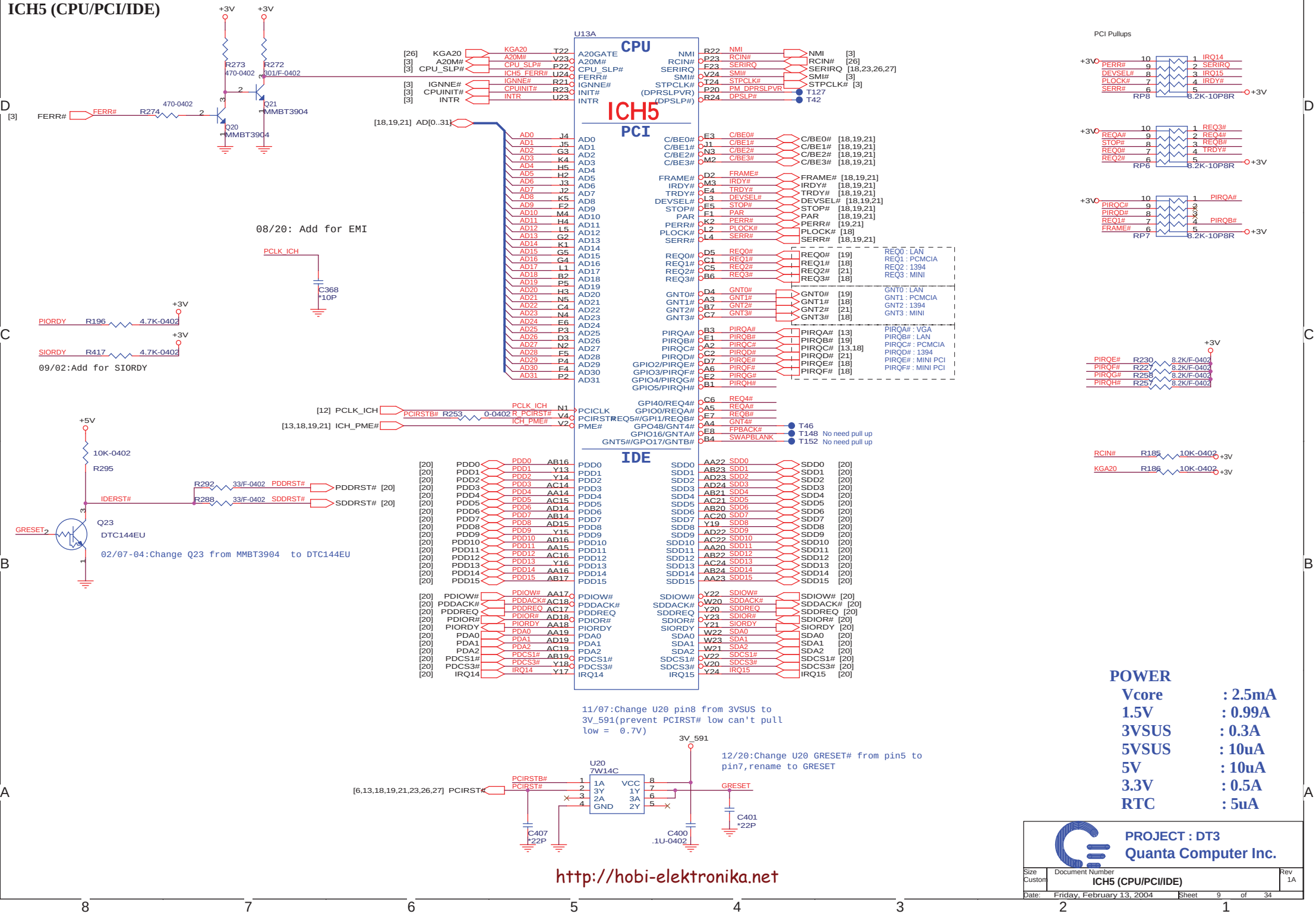
MCH (DDR A & DDR B)



ICH5 (CPU/PCI/IDE)



POWER

Vcore	: 2.5mA
1.5V	: 0.99A
3VSUS	: 0.3A
5VSUS	: 10uA
5V	: 10uA
3.3V	: 0.5A
RTC	: 5uA

PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	ICH5 (CPU/PCI/IDE)	1A
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ICH5 (USB/HUB/LPC/AC97)

12/30:PCSPK(functional strap)
H:No Reboot Mode
L:Normal Mode

12/27:AC_SDOUT(functional strap)
H:Safe Mode
L:Normal Mode

12/27:Add R480 for SLP_S5# pull high

10/28:Change R229,R222 from 33-0402 to 10-0402(for AC 97Transition time)

12/27:Add R262(CS00002JB03)

12/27:Change R446 from 100k to 10k

USB 0 to Card Reader
USB 1 to MDC
USB 2,3,4,5 to SYSTEM
USB 6,7 to Reverse

10MIL TRACE/ 7MIL SPACE

0.8*VDD (1.5V) +/- 2%

0.233*VDD (1.5V) +/- 2%

08/20:Add for EMI

RESISTOR DIVIDER PLUS 1 CAP PLACED AT MIDPOINT OF BUS. TWO OTHER CAPS PLACED 1 EA AT MCH AND ICH USE 10MIL TRACE, ISOLATE W/ 7MIL SPACE

10/06:Change C339,C340 value from 10P to 15P(for Xtal precision)

12/30:Change C377 to R479

PROJECT : DT3
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Size	Document Number	Rev
Custom	ICH5 (USB/HUB/LPC/AC97)	1A
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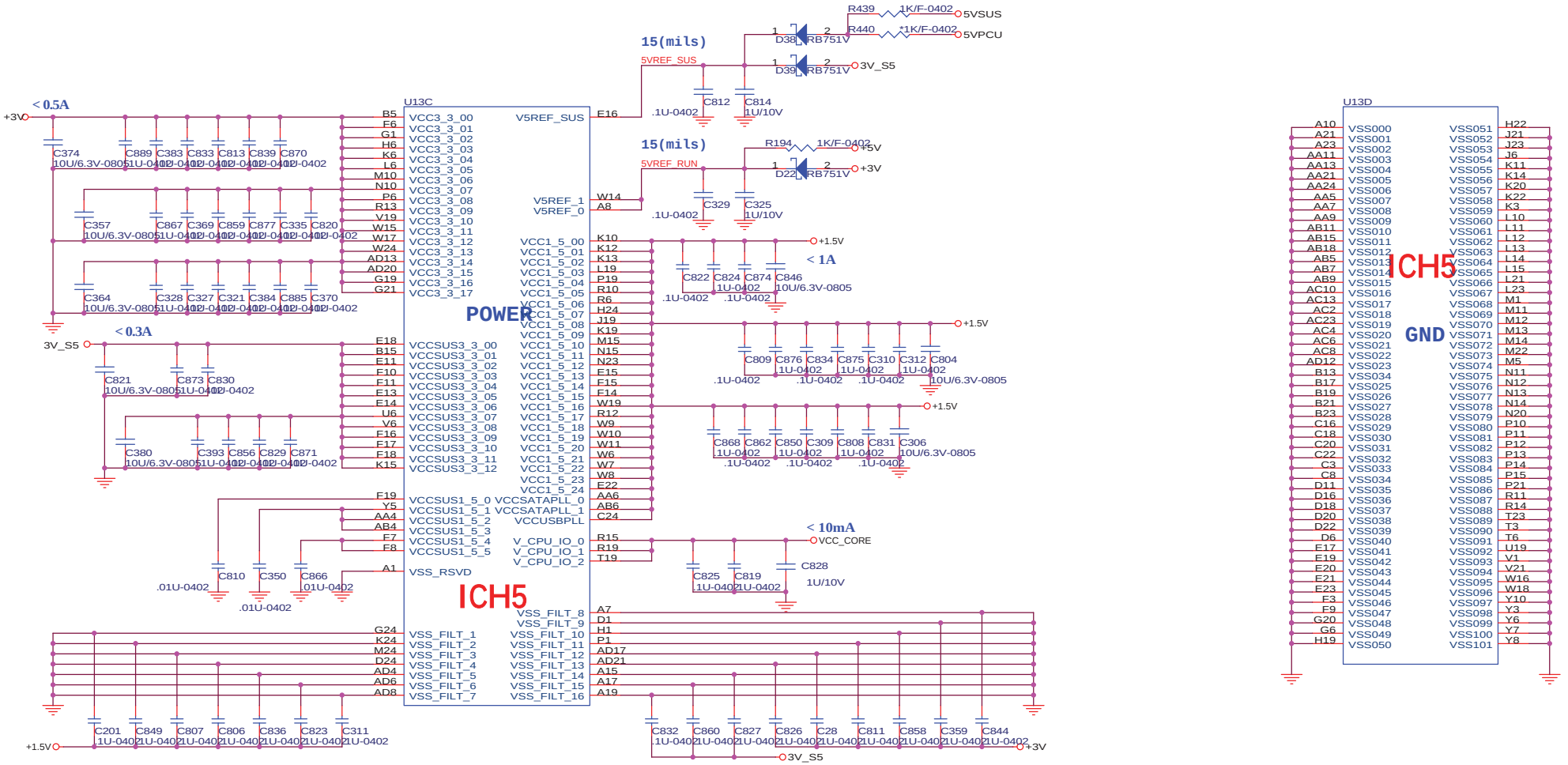
ICH5 POWER



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PROJECT : DT3			
Quanta Computer Inc.			
Size: Custom	Document Number: ICH5 POWER		Rev: 1A
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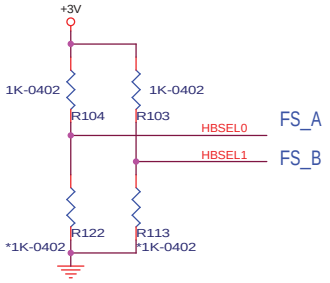
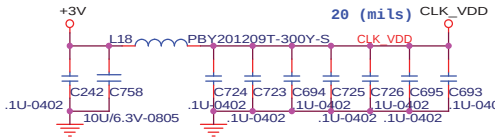
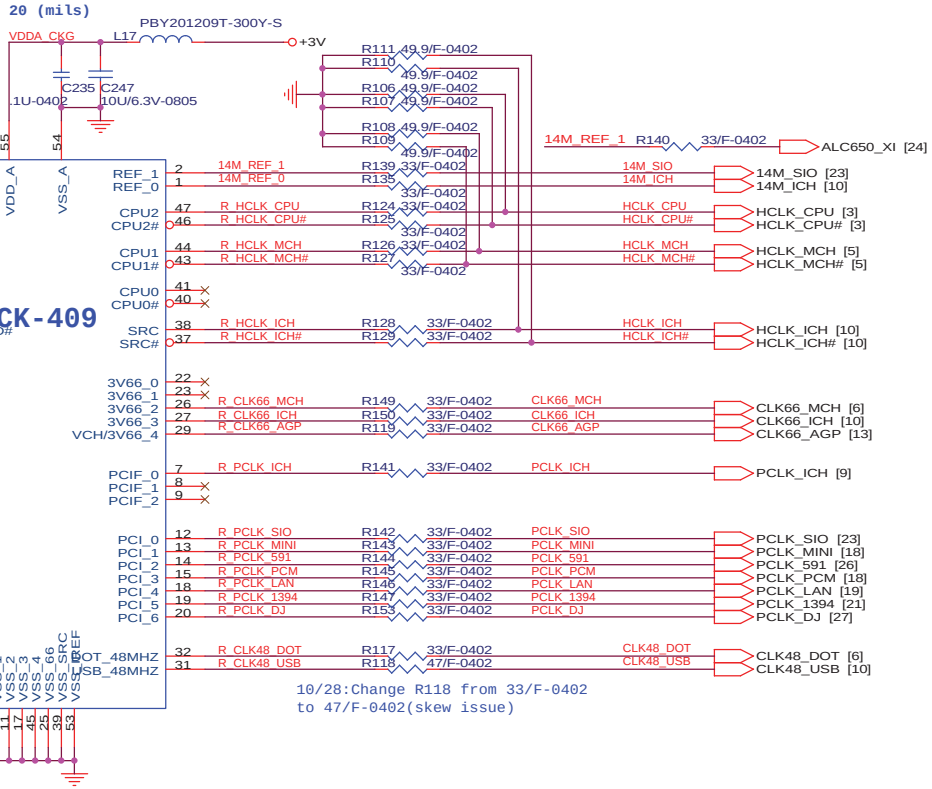
Clock Generator

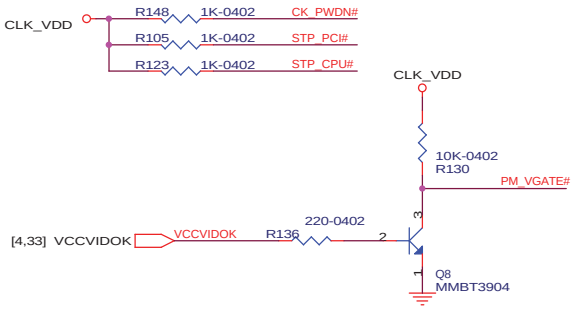
FSA	FSB	CPU
0	0	100MHZ HOST CLOCK
1	0	133MHZ HOST CLOCK
1	1	166MHZ HOST CLOCK
0	1	200MHZ HOST CLOCK

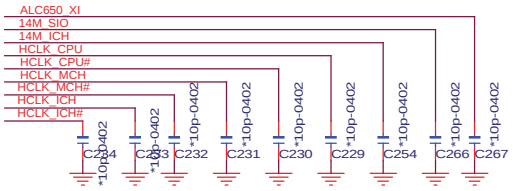


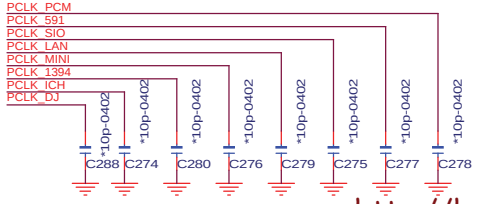
08/25:Operating Supply Current Max:350mA

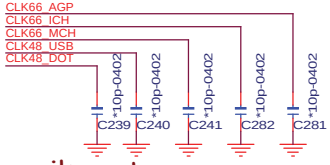












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Custom	Clock Generator	1A
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AGP 8X OUT INTERFACE

AGP_PLUG:
HI: USE INTERNAL GRAPHIC
LO: USE EXTERNAL GRAPHIC

6/5: AGPDET- used for GCDET- (AGP 8X)

AGPDET- : HI use VB board (1.5V)
AGPDET- : LO use VGA board (1.5V)

09/02: Add for EMI

10/20:Change R423 value from 33-0402 to 10-0402

6/5: Add R pull AGP_PERR# to GND

PLACE CLOSE TO CONNECTOR

6/5: Change R293 to 124/F-0402, R297 to 38/F-0402 for AGP 8X = 0.35V

6/5: Add switch for AGP card

02/26 +5VSUS CHANGE TO 5V

09/01:Chnage from LCD_ON to +1.5V

09/01:Change from SUSON to +5V

09/02:Change from AGPDET- to TP

NV31:
1.5V 1A
2.5V 6A
VCORE(1.25V) 10A

LCD Panel	LCD ID2 SB PANEL_ID2	LCD ID1 SB PANEL_ID1	LCD ID0 SB PANEL_ID0
17" SXGA	1	1	1
15" XGA	0	1	1

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PROJECT : DT3
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Custom	AGP SLOT	1A

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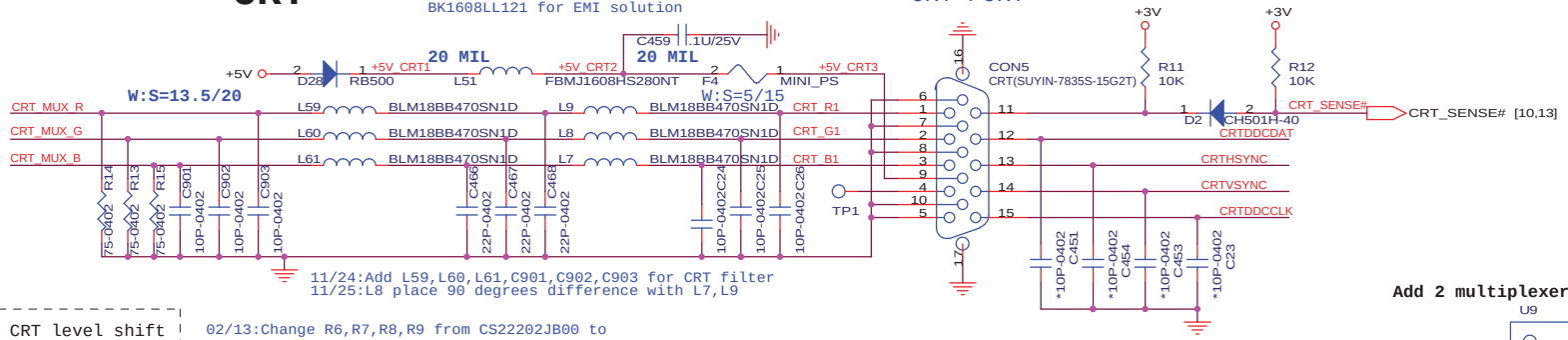
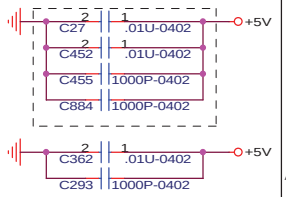
CRT

11/17:Change L7,L8,L9 value from BK1608LL680 to BK1608LL121 for EMI solution

CRT PORT

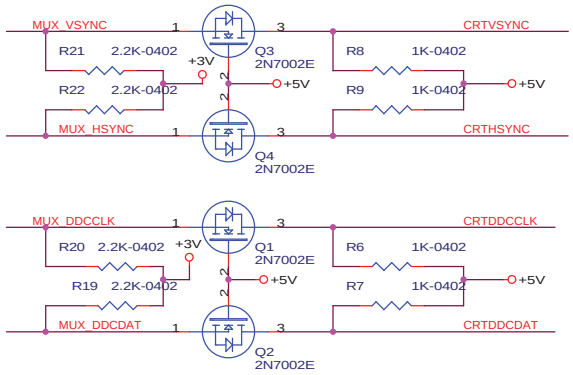




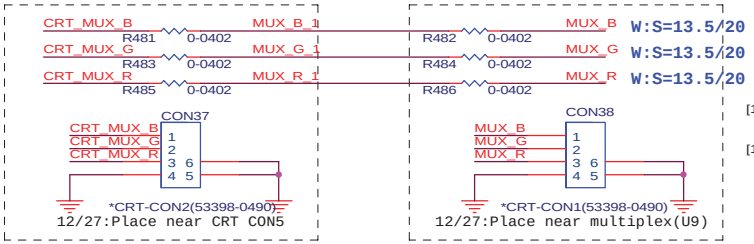
11/24:Add L59,L60,L61,C901,C902,C903 for CRT filter
11/25:L8 place 90 degrees difference with L7,L9

6/17: Add CRT level shift

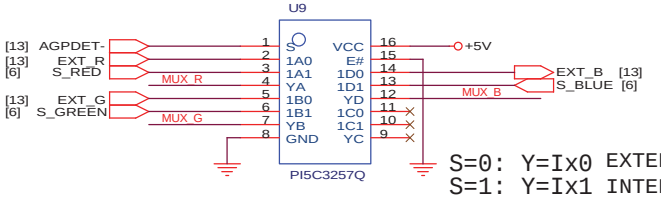
02/13:Change R6,R7,R8,R9 from CS22202JB00 to CS21002JB00(Meet different LCD specs)



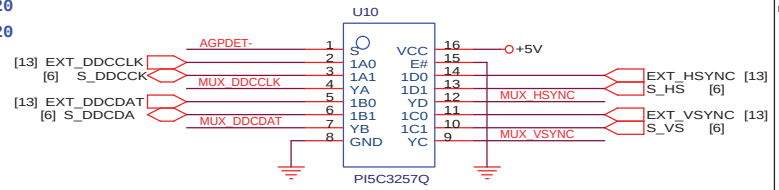
12/27:Add CON37,CON38,R481,R482,R483,R484,R485,R486 for prevent CRT flicker



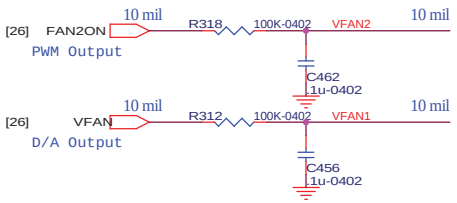
Add 2 multiplexer for CRT and TV swap



Video AND CRT MUXED SIGNALS

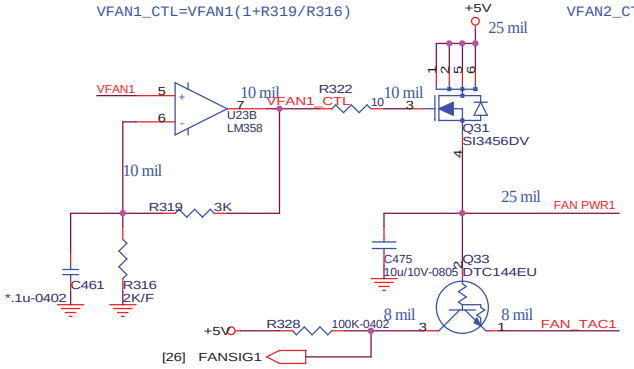


FAN Control



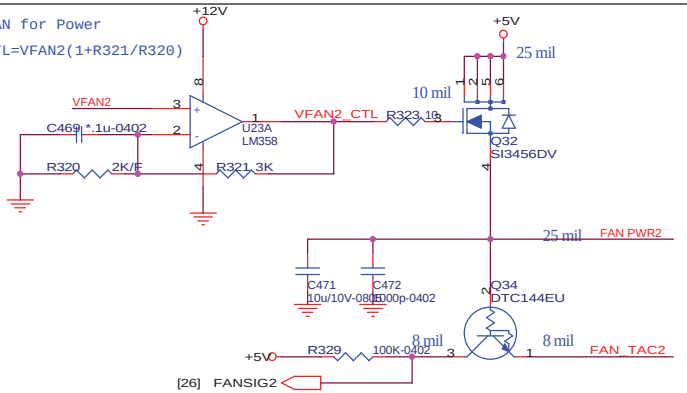
Big FAN for CPU

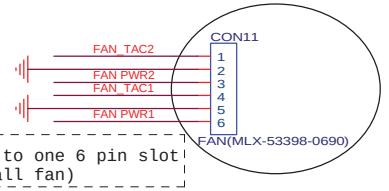
VFAN1_CTL=VFAN1(1+R319/R316)



Small FAN for Power

VFAN2_CTL=VFAN2(1+R321/R320)





6/12: Change two 3 pin slots to one 6 pin slot (123 for big fan, 456 for small fan)

LCD-LID

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Size	Document Number	Rev
Custom	CRT,LCD LID,BLON & FAN CONTROL	1A
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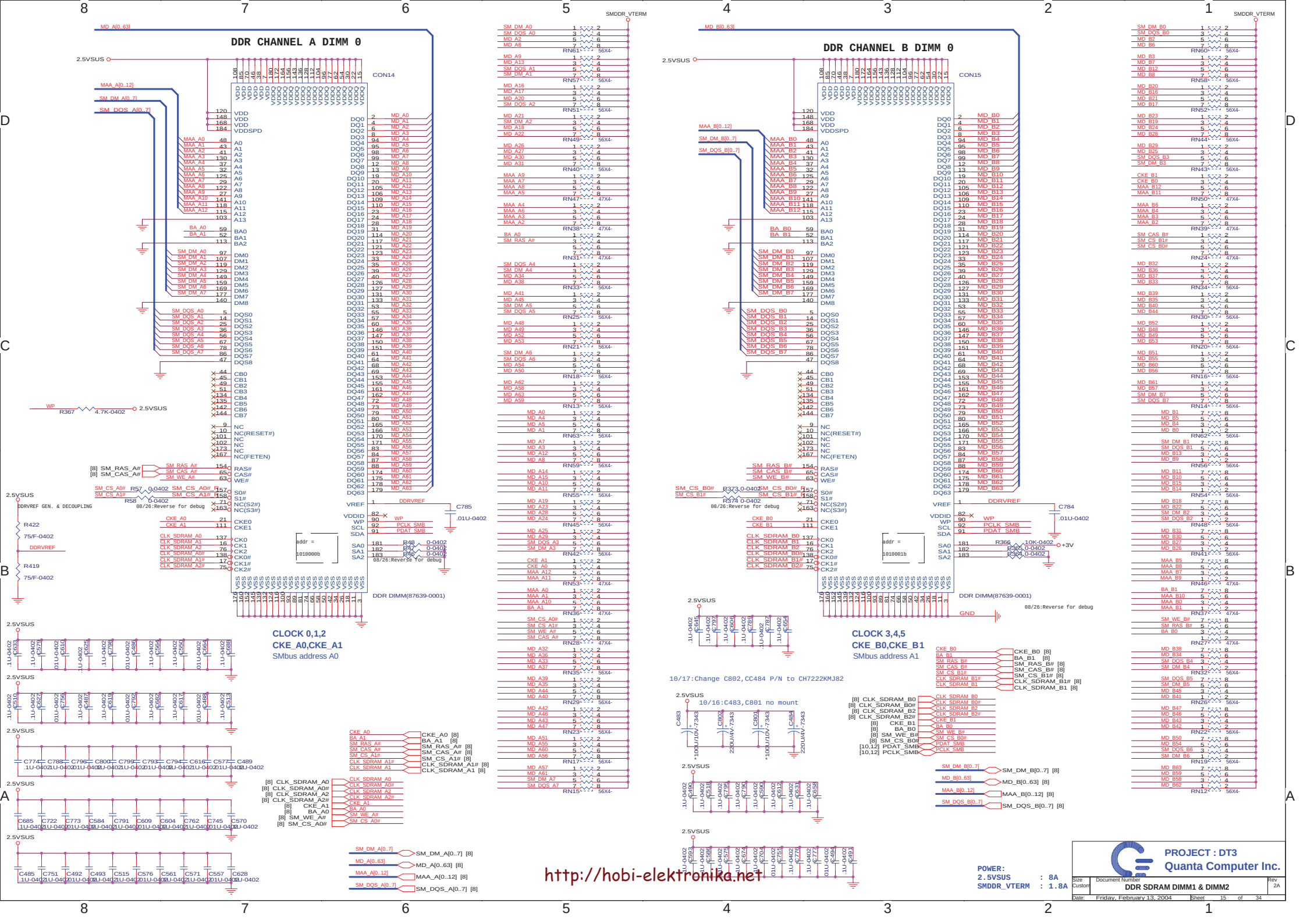
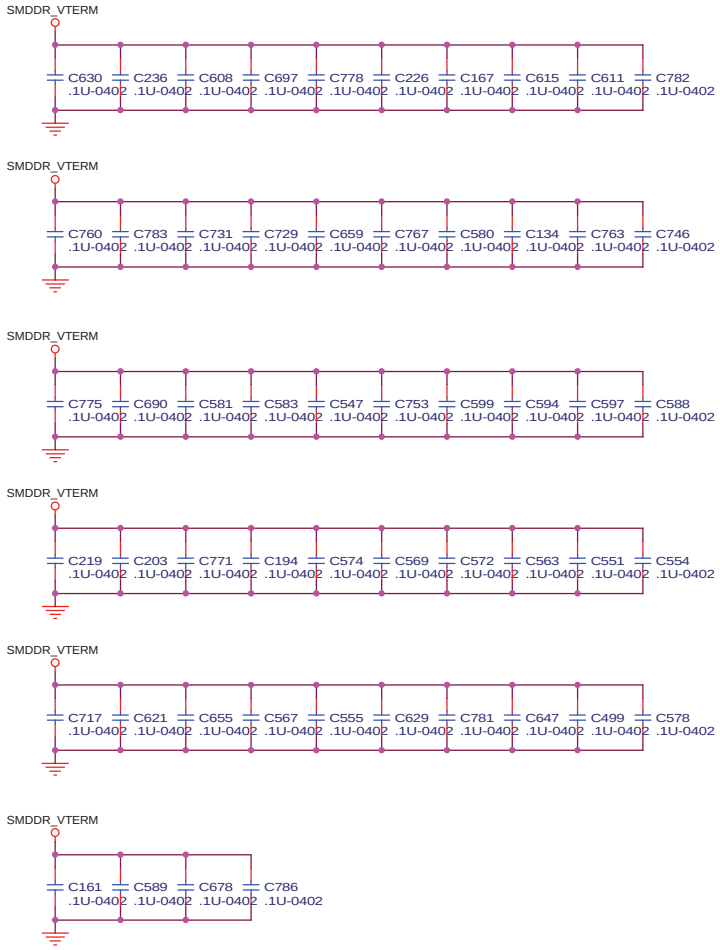
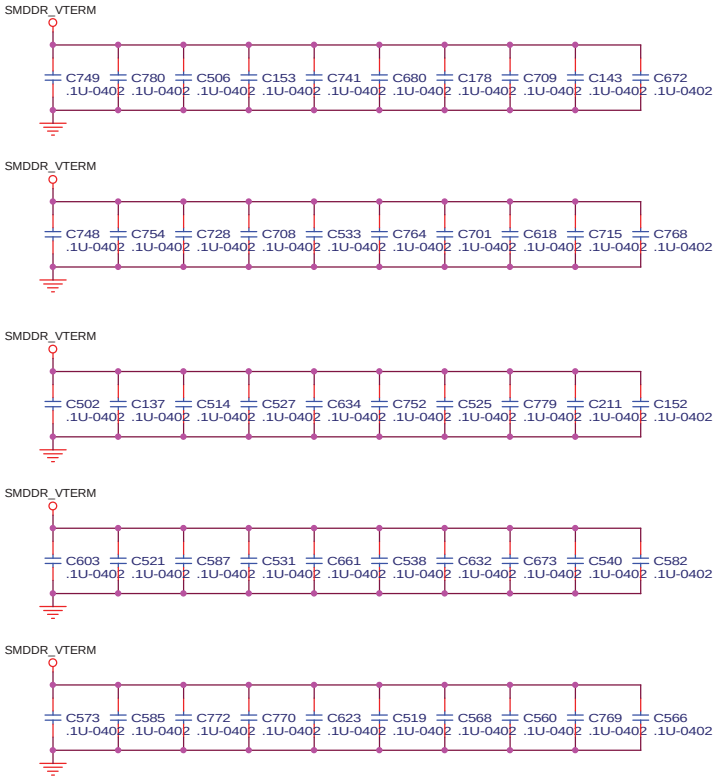

DDR CHANNEL A DIMM 0
DDR CHANNEL B DIMM 0
CON14
CON15
DDR DIMM(87639-0001)
SMDDR_VTERM
2.5VSUS
DDRVREF
CLOCK 0,1,2
CKE_A0,CKE_A1
SMbus address A0
CLOCK 3,4,5
CKE_B0,CKE_B1
SMbus address A1
10/17:Change C802,CC484 P/N to CH7222KMJ82
10/16:C483,C801 no mount
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
DDR SDRAM DIMM1 & DIMM2
POWER:
2.5VSUS : 8A
SMDDR_VTERM : 1.8A

DDR CHANNEL A&B RESISTOR TERMINATION





PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	SSTL-2 TERMINATION RESISTORS	1A
Date: Friday, February 13, 2004	Sheet 16 of 34	

USB PORT

6/5: Remove common mode choke (CML6, CML8) to short

CML9 RFCMF1632140M2T
CML8 *PLW3216S900SQ2B1
CML2 RFCMF1632140M2T
CML1 RFCMF1632140M2T
CML7 *PLW3216S900SQ2B1
CML10 RFCMF1632140M2T

5VSUS → L52 BK1608HS600 → 5VSUS_IN1 → F1 POLY SWITCH-1.5A (1206) (SMD1206P150TS) → 5VSUS_OUT1

6/12: Change C29, C173 from T47U/6.3V to 100U_25V

6/5: Change F1, F3 from fuse to poly switch

5VSUS → L16 BK1608HS600 → 5VSUS_IN2 → F2 POLY SWITCH-1.5A (1206) (SMD1206P150TS) → 5VSUS_OUT2

[10] USBOC0# → R326 10K-0402 → 60 MIL → C30 100U_25V

6/5: Delete R386 & change R388 from 0 to 10K-0402

R327 560K/F, C470 1000P-0402

[10] USBOC1# → R389 10K-0402 → 60 mils → C164 100U_25V

10/16:Change footprint from 2522A-08GXT-8P-V to 2522A-08GXT-8P-H(drill with pad issue)

6/5: Delete R425 & change R423 from 0 to 10K-0402

R390 560K/F, C624 1000P-0402

CON7 — USB(SUYIN) 2522A-08GXT-8P-V

[10] USBP5-/USBP5+ → BUSBP5-/BUSBP5+ → R25 *0 / R23 *0 → CON_BUSBP5-/CON_BUSBP5+ → C33 10P-0402, C34 10P-0402

09/03:CM choke co-layout with resister

[10] USBP4-/USBP4+ → BUSBP4-/BUSBP4+ → R309 *0 / R310 *0 → CON_BUSBP4-/CON_BUSBP4+ → C441 10P-0402, C436 10P-0402

09/03:CM choke co-layout with resister

[10] SYSUSBP2-/SYSUSBP2+ → BUSBP2-/BUSBP2+ → C156 10P-0402, C159 10P-0402

09/03:CM choke co-layout with resister

[10] SYSUSBP3-/SYSUSBP3+ → BUSBP3-/BUSBP3+ → C601 10P-0402, C602 10P-0402

09/03:CM choke co-layout with resister

CON13 — USB(SUYIN) 2522A-08GXT-8P-H

60 mils → R74 *0 / R72 *0 → CON_BUSBP2-/CON_BUSBP2+ → R372 *0 / R375 *0 → CON_BUSBP3-/CON_BUSBP3+

11/17:Mount C33,C34,C156,C159,C305,C307,C314,C315,C441,C436,C601,C602 for EMI solution(USB)

[10] SYSUSBP1-/SYSUSBP1+ → BUSBP1-/BUSBP1+ → R289 0 / R284 0 → USB1-/USB1+ → USB1- [18] / USB1+ [18]

C307 10P-0402, C305 10P-0402

09/03:CM choke co-layout with resister

6/5: for MDC

[10] SYSUSBP0-/SYSUSBP0+ → BUSBP0-/BUSBP0+ → R282 0 / R287 0 → USB0-/USB0+ → USB0- [18] / USB0+ [18]

C314 10P-0402, C315 10P-0402

09/03:CM choke co-layout with resister

6/5: for card reader

[10] USBP6-/USBP6+ → BUSBP6-/BUSBP6+ → R441 *0 / R438 *0 → CON_BUSBP6-/CON_BUSBP6+ → T128, T126

C816 *10P-0402, C817 *10P-0402

[10] USBP7-/USBP7+ → BUSBP7-/BUSBP7+ → R195 *0 / R206 *0 → CON_BUSBP7-/CON_BUSBP7+ → T44, T45

C334 *10P-0402, C326 *10P-0402

LED/B CONNECTOR

CONNECT TO LED/B

10/09:Add BT_LED for LED board pin 4

CON33 — LED(52271-2690)

1	BATLED0	BATLED0 [26]
2	BATLED1	BATLED1 [26]
3	RFLED	
4	BT_LED	BT_LED [18]
5		T105
6	GET-MAIL-	GET-MAIL- [10]
7	LID#	LID# [14,26]
8	3V_591	3V_591
9		T116
10	SUS_LED	SUS_LED [26]
11	3VSUS	3VSUS
12	+3V	+3V
13	-NBSWON	-NBSWON [26]
14	RF_ON#	RF_ON# [26]
15	QSW_P2	QSW_P2 [26]
16	QSW_P1	QSW_P1 [26]
17	QSW_IE	QSW_IE [26]
18	QSW_MAIL	QSW_MAIL [26]
19		T118
20		
21		
22	HDDLED-	HDDLED- [20]
23	CAPSLED#	CAPSLED# [26]
24	SCROLED#	SCROLED# [26]
25	NUMLED#	NUMLED# [26]
26		

09/22:change footprint from 6701-04-4p-R to 6701-04

TP/B CONNECTOR

CON34 — TP/B-CON(4601-04)

TP-CLK, TP-DAT, +5V, GND → C765 .1U-0402

RF_ON# = QSW_WLAN/QSW_BT

6/18: Change TP/B power from +5VSUS to 5V

TP-DAT → L57 BK1005HM241 → TPDATA → TPDATA [26]

TP-CLK → L56 BK1005HM241 → TPCLK → TPCLK [26]

RFLED → R473 *0 → RF_LED [18]

R474 0 → RF_ENABLE [18,26]

11/19:Add R473,R474 for WLAN LED select.Mount R473(WLAN Card).Mount R474(EC control)

+5V → R313 CPU-THERMTRIP- 100K-0402 → Q29 MMBT3904

[3,10] THERMTRIP# → R314 CPU-TRIP- 68K-0402 → Q30 MMBT3904

RESET- → RESET- [30]

POWER SW

3V_591 → R311 1K-0402 → SW2 → POWER SWITCH

[26] -NBSWON → C450 1U

PS/2

CON1 35136S-06T1 PS2(SUYIN-35236S-06T1-FK)

K-DATA, M-DATA, GND, +5V, K-CLK, M-CLK

+5V → C3 .1U-0402

RESET SW

RESET- → SW1 → RESET SWITCH

[26] MSDATA — MDATA L2 BK1005HM241 M-DATA C438 47P-0402

[26] KBDATA — KDATA L3 BK1005HM241 K-DATA C439 47P-0402

[26] MSCLK — MCLK L1 BK1005HM241 M-CLK C437 47P-0402

[26] KBCLK — KCLK L4 BK1005HM241 K-CLK C440 47P-0402

NEAR CON8

-NBSWON								+3V
RF_ON#		C730	*.1U-0402		C720	.1U-0402		3V_591
HDDLED-	C732	220P-0402		C706	.1U-0402			3VSUS
CAPSLED#		C757	220P-0402		C716	*.1U-0402		BATLED0
SCROLED#	C759	220P-0402		C679	*.1U-0402			BATLED1
NUMLED#		C761	220P-0402		C683	*.1U-0402		RFLED
QSW_MAIL	C766	220P-0402		C686	220P-0402			GET-MAIL-
QSW_IE		C747	220P-0402		C699	220P-0402		LID#
QSW_P1	C742	220P-0402		C702	220P-0402			SUS_LED
QSW_P2		C739	220P-0402		C711	220P-0402		BT_LED
	C738	220P-0402		C893	220P-0402			

10/16:Add C893

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Quanta Computer Inc.

Size	Document Number	Rev
Custom	USB,LED/B,PS2,PW & RST CONN	2A
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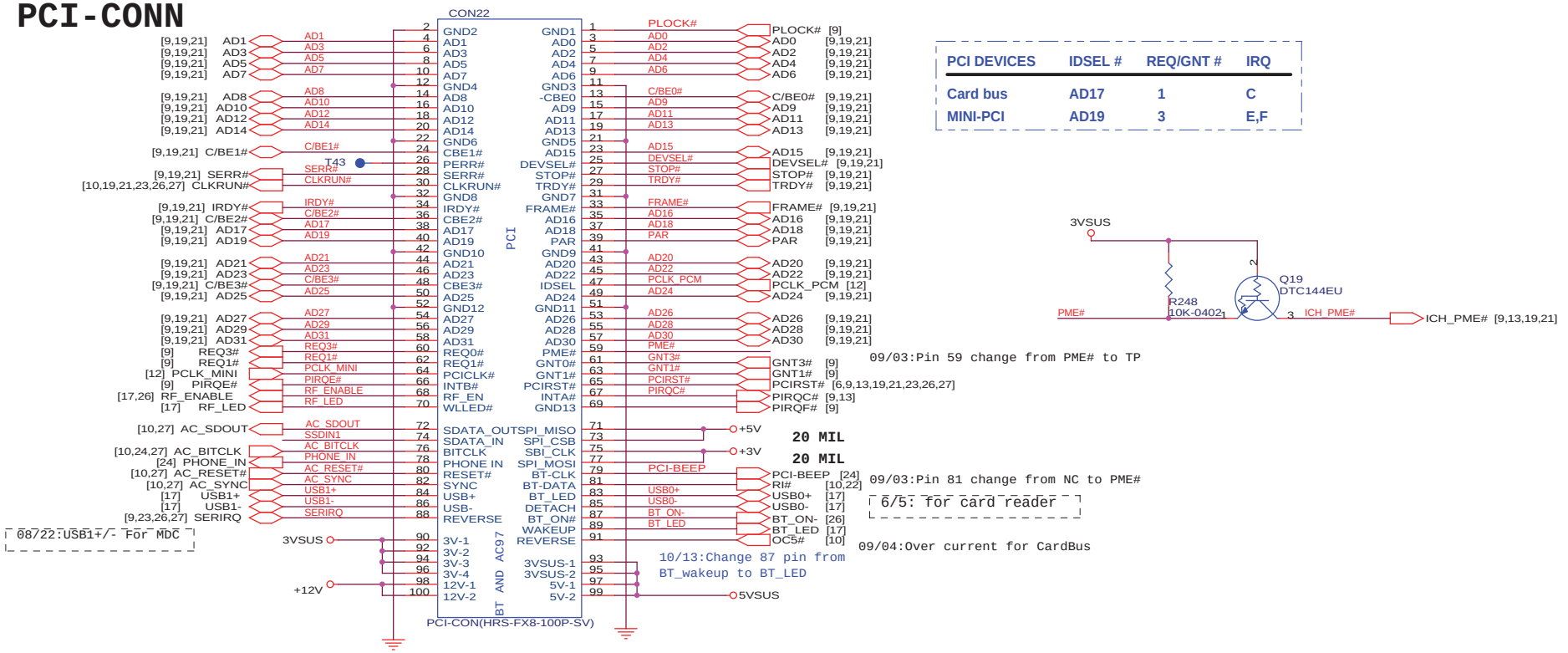
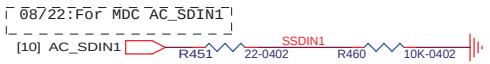
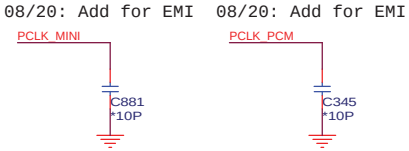
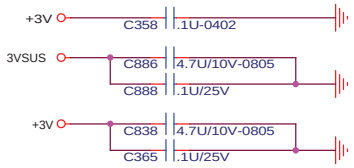
PCI-CONN



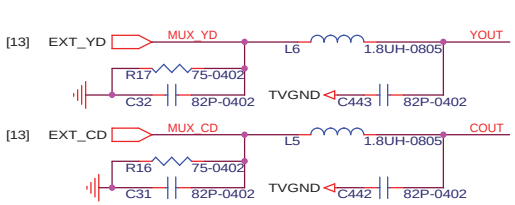
PCI DEVICES	IDSEL #	REQ/GNT #	IRQ
Card bus	AD17	1	C
MINI-PCI	AD19	3	E,F

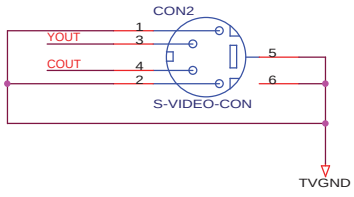


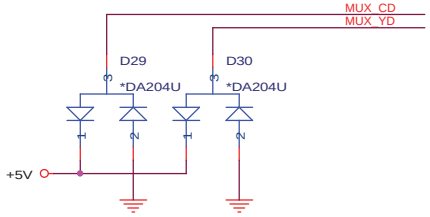




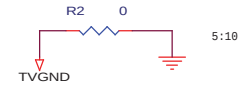
S-VIDEO(TV OUT)







PSOT05LC



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PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	PCI CONN & S-VIDEO	1A
Date: Friday, February 13, 2004	Sheet 18 of 34	

Pair

LAN (Boardcom BCM5788M)

ID Select	: AD16
Interrupt Pin	: PIRQB#
Request indicates	: REQ0#
Grant indicates	: GNT0#

BCM5788M

15mm x 15mm
BGA196

10/23:Change L12 from TB160808G601 to TB160808B601(Stock issue)

10/23:Change L14 from TB160808G601 to TB160808B601(Stock issue)

08/27:(10)For RDAC, use 1.27K 1% for 4401, 1.24K 1% for BCM5705

08/25:Change to 1.0K

10/22:Change U26 P/N from AKE3B1NKE05 to AKE3N8BKA04(AT24C128-10TI-2.7)

10/06:Change CON9 footprint from jfm3131x-3320w-14p to jfm3131x-3330w-14p
10/17:Change P/N to DFTJ10FR101

10/15:10/100 Mbps Yellow LED
1000Mbps Green LED

08/27:If use CLKRUN#,mount R398
If not use CLKRUN#,mount R397

1.5" AWAY FROM CHIP
Use Philips BCP69-16, hfe=75~275

BCM4401 is for 10/100(1.8)
BCM5702 is for giga
BCM5705M is for giga cost-down(12)

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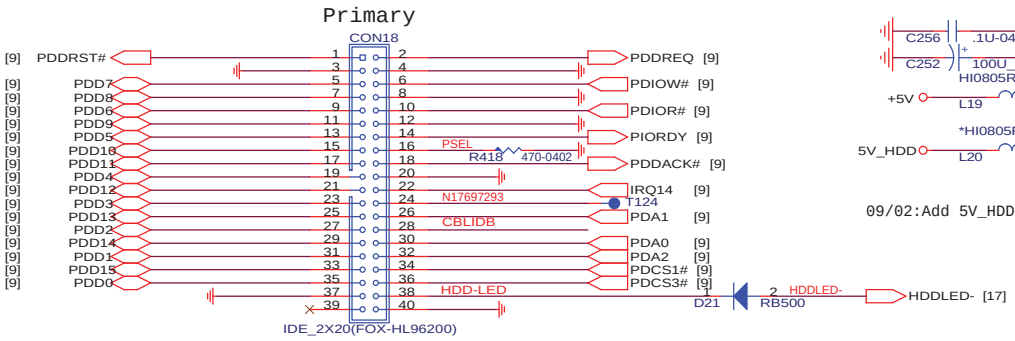
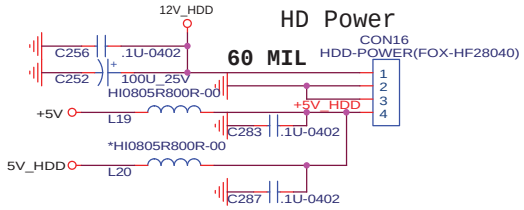
PROJECT : DT3 Quanta Computer Inc.			
Size: Custom	Document Number: LAN (Boardcom BCM5788M) & RJ45,RJ11		Rev: 2A
Date: Friday, February 13, 2004	Sheet 19 of 34		

HDD,CD-ROM CONNECTOR

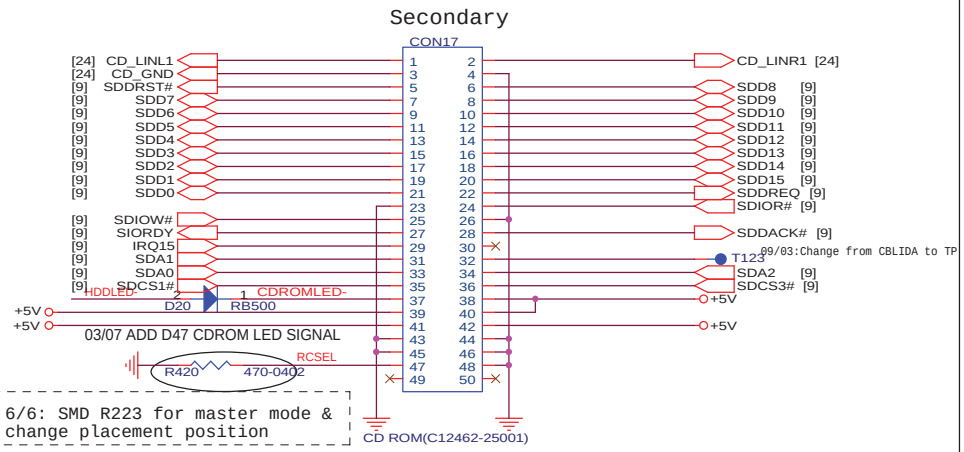


HDD POWER CONNECTOR



09/02:Add 5V_HDD for test

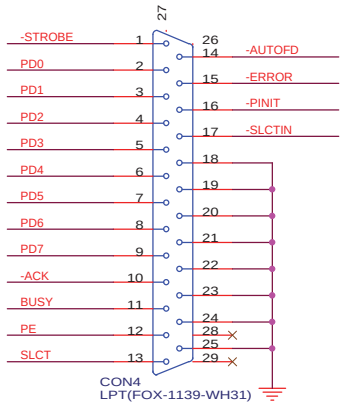
CD-ROM connector



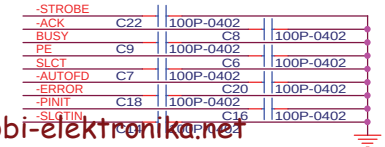
6/6: SMD R223 for master mode & change placement position

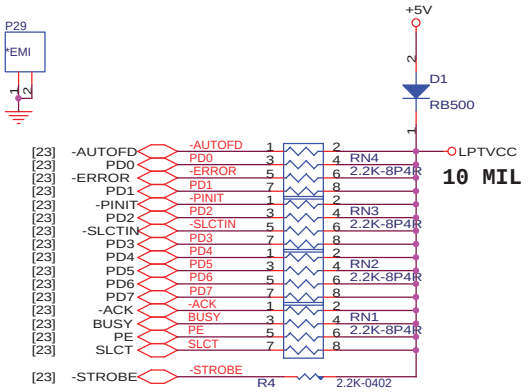
JEDEC spec:
pull low: Master mode
open: C/S mode

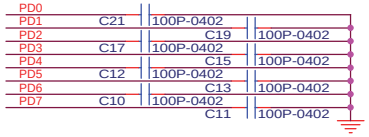
LPT CONN.



11/17:Cut LPT CONN pin 28,29 with GND.It is PLATED HOLE.







PROJECT : DT3
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Size	Document Number	Rev
Custom	IDE (HDD & CDROM) & LPT	1A
Date: Friday, February 13, 2004	Sheet 20 of 34	

1394 (TSB 43AB22)

ID Select	:	AD18
Interrupt Pin	:	PIRQD#
Request indicates	:	REQ2#
Grant indicates	:	GNT2#

AD18 R224 100-0402 IDSEL1394

1394_3V C333 .1U C853 .1U C837 .01U C878 .01U C341 1000P

Used for vccp
(Pin20,35,48,62,78)

60-ohms @ 100MHz

1394

1394_3V L58 BK1608HS600 30 mil PLLVCC C863 1000P C865 10U-1206

1394_3V C343 .1U C336 .1U C845 .1U C338 .01U C842 .01U C861 1000P C352 1000P C344 1000P

Used for vcc3
(Pin15,27,39,51,59,72,88,100)

1394_3V C337 .1U C848 .1U C332 .01U C331 1000P C342 1000P

Used for AVCC
(Pin1,2,107,108,120)

09/12:Must be place
between 1394_3V and +3V

1394_3V 30 mil Via : 2 (24/12) R188 0-0805 R189 0-0805 +3V 30 mil Via : 2 (24/12)

09/12:Must be place
between AGND_1394 and GND

C818 1000P-0402 C805 1000P-0402 C883 1000P-0402 R434 0-0805 R437 0-0805 AGND_1394

U14
TSB43AB22A

[9,18,19] AD[0..31]

PCI INTERFACE

AD0 AD1 AD2 AD3 AD4 AD5 AD6 AD7 AD8 AD9 AD10 AD11 AD12 AD13 AD14 AD15 AD16 AD17 AD18 AD19 AD20 AD21 AD22 AD23 AD24 AD25 AD26 AD27 AD28 AD29 AD30 AD31

[9,18,19] C/BE0#
[9,18,19] C/BE1#
[9,18,19] C/BE2#
[9,18,19] C/BE3#
[12] PCLK_1394
[9] GNT2#
[9] REQ2#
IDSEL1394
[9,18,19] FRAME#
[9,18,19] IRDY#
[9,18,19] TRDY#
[9,18,19] DEVSEL#
[9,18,19] STOP#
[9,19] PERR#
[9,18,19] SERR#
[9,18,19] PAR
PME1394#
[9] PIRQD#
[10,18,19,23,26,27] CLKRUN#
[6,9,13,18,19,23,26,27] PCIRST#

CBE0- CBE1- CBE2- CBE3- PCI_CLK GNT- REQ- IDSEL FRAME- IRDY- TRDY- DEVSEL- STOP- PERR- SERR- PAR PCI_PME- INTA- CLKRUN- RST- G_RST- GPIO2 GPIO3

1394_3V R444 *100K-0402 R448 0-0402

10 mil

C847 *.1U-0402 R203 220-0402 R204 220-0402

PHY PORT 0: TPBIAS0 TPA0+ TPA0- TPB0+ TPB0-
BIAS CURRENT: R0 R1
CRYSTAL: X0 X1
FILTER: F0 F1
EEPROM BUS: SDATA SCLK
PHY PORT 1: TPBIAS1 TPA1+ TPA1- TPB1+ TPB1-
POWER CLASS: PC0 PC1 PC2

TEST7 TEST16 TEST17 CYCLEIN CYCLEOUT CPS
TEST9 TEST8 TEST3 TEST2 TEST1 TEST0

1394_3V Pullup U5#86 (CYCLEOUT) with 10K to +3V:Add R583

R197 4.7K-0402 R445 4.7K-0402 R459 4.7K-0402 R205 4.7K-0402 R443 10K-0402
0822:TEST17 Pull up VCC
R217 1K-0402

TPBIA0
1394_TPA0+ 1394_TPA0- 1394_TPB0+ 1394_TPB0-

1394_R0 R223 6.34K/F-0402
1394_R1
X0 C356 20P Y5 24.576MHz X1 C360 20P
1394_F0 C348 .1U
1394_F1

1394_SDATA 1394_SCLK

TPBIA1
1394_TPA1+ 1394_TPA1- 1394_TPB1+ 1394_TPB1-

R199 0-0402 R198 0-0402 R209 0-0402 R211 0-0402 R214 0-0402 R215 0-0402

R232 56.2/F-0402 R235 56.2/F-0402 TP1 R453 5.11K/F C841 220P-0402 AGND_1394

C852 1U R231 56.2/F-0402 R228 56.2/F-0402 AGND_1394

C869 1U R219 56.2/F-0402 R213 56.2/F-0402

R221 56.2/F-0402 R225 56.2/F-0402 TP2 R456 5.11K/F C855 220P-0402 AGND_1394

10/06:Change C356,C360 value from 12P to 20P(for Xtal precision)

10/16:Change footprint from UV31413-4RA-4P-V to UV31413-4RA-4P-H(drill with pad issue)

09/03:CM choke co-layout with resister

1394_TPA0+ L34 1394_TPA0-
1394_TPB0+ L36 1394_TPB0-
1394_TPA0+ L33 1394_TPA0-
1394_TPB0- L35 1394_TPB0-

CML5 *RFCMF1632140MT2
CML6 *RFCMF1632140MT2

10/09:CON21 pin 5,6 change from AGND_1394 to GND(EMI Issue)

CON21 L1394_TPB0- L1394_TPA0- L1394_TPA0+ L1394_TPB0+
1394-4P(FOX-UV1413-4RA)

C882 *22P C872 *22P C864 *22P C880 *22P

09/03:CM choke co-layout with resister

1394_TPA1+ L30 1394_TPA1-
1394_TPB1+ L32 1394_TPB1-
1394_TPA1+ L29 1394_TPA1-
1394_TPB1- L31 1394_TPB1-

CML3 *RFCMF1632140MT2
CML4 *RFCMF1632140MT2

C854 *22P C857 *22P C835 *22P C843 *22P

40 MIL BK1608HS600 POLY SWITCH-1.5A

VIN D23 EP05FA20 1394VCC L28 1394VCC_R F3 C322 .1U-0402

C330 1U C815 1U AGND_1394

CON20 1394-6P(FOX-UF3161X-6P)
1394-VCC 1394-VSS L1394_TPB1- L1394_TPB1+ L1394_TPA1- L1394_TPA1+

10/16:Change footprint from UF3161X-H1-6P to uf3161x-h1-6p-h(drill with pad issue)

1394_GND 1394_GND_1

C354 .1U C355 .01U R233 *0-0402 C323 .1U C324 .01U R200 *0-0402

AGND_1394

1394_3V R202 2.7K-0402 R201 2.7K-0402

1394_SCLK 1394_SDATA

U12 SCL SDA WP A0 A1 A2 VCC GND AT24C02

C353 .1U

P15 *EMI

1394_3V 6 mil R458 10K-0402 Q38 DTC144EU PME1394# ICH_PME# ICH_PME# [9,13,18,19]

10/16:Change P/N from AKE3K8S0A00 to AKE130800A6

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PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	IEEE 1394(TSB 43AB22A)	1A

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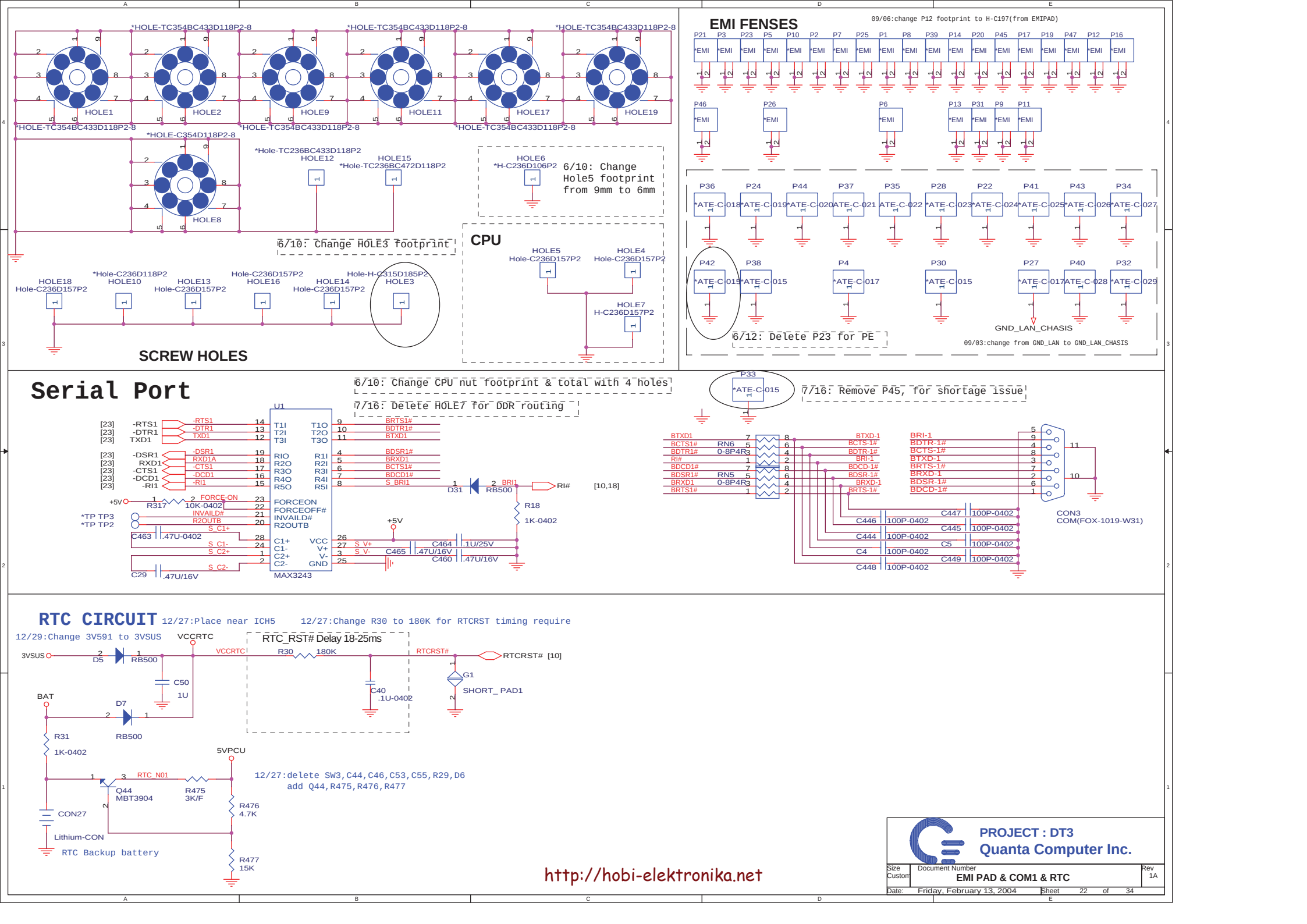
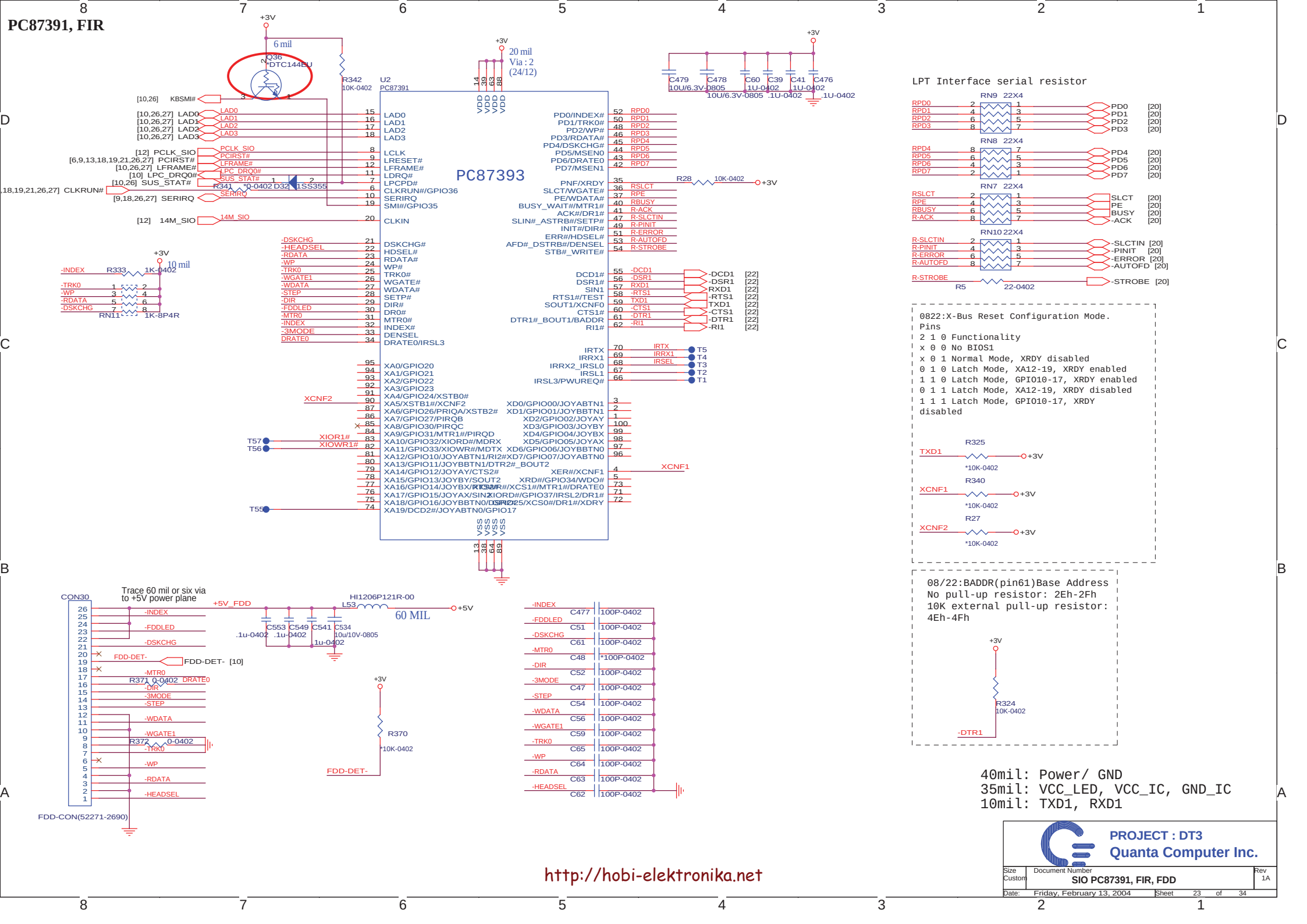
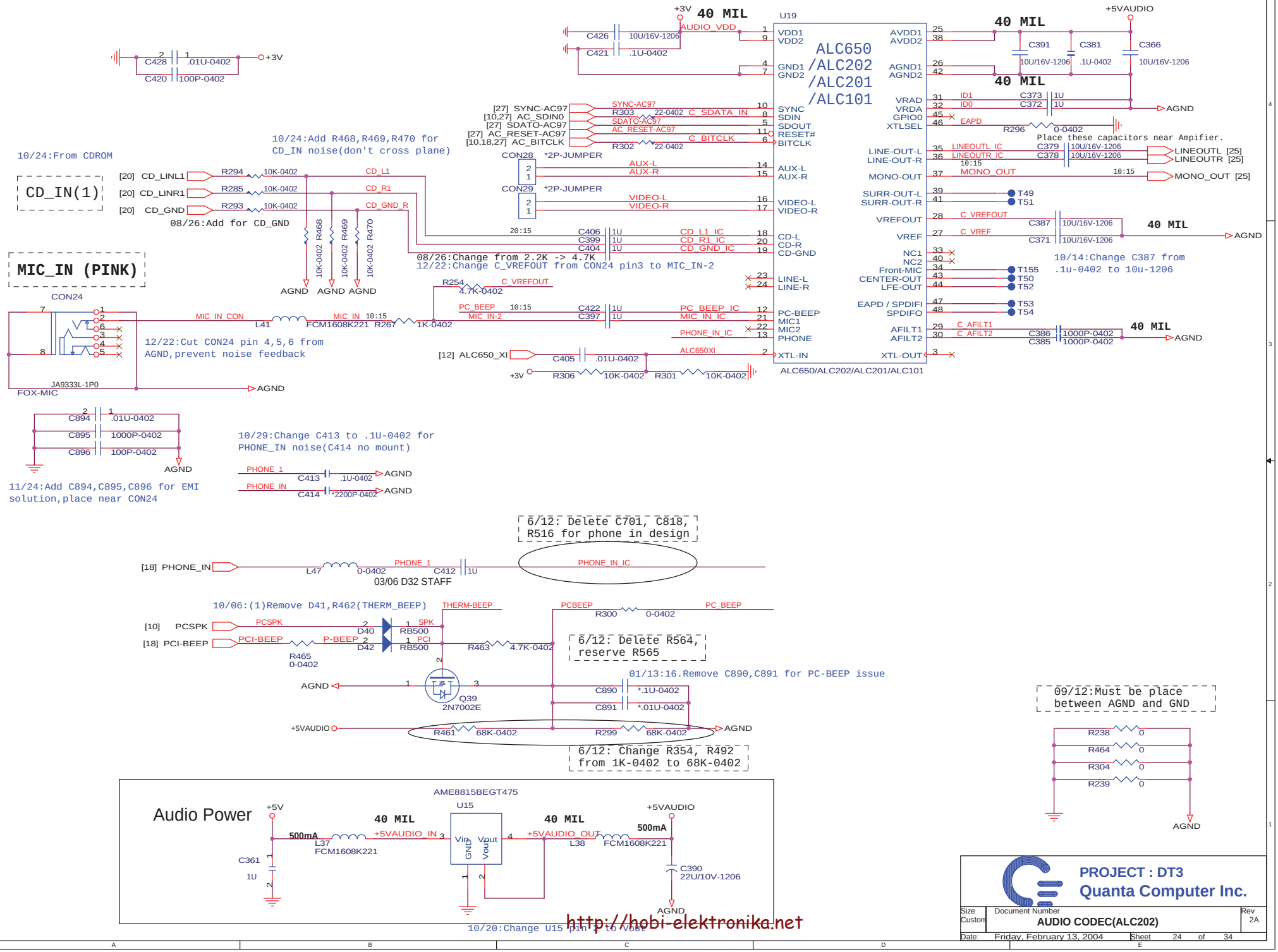
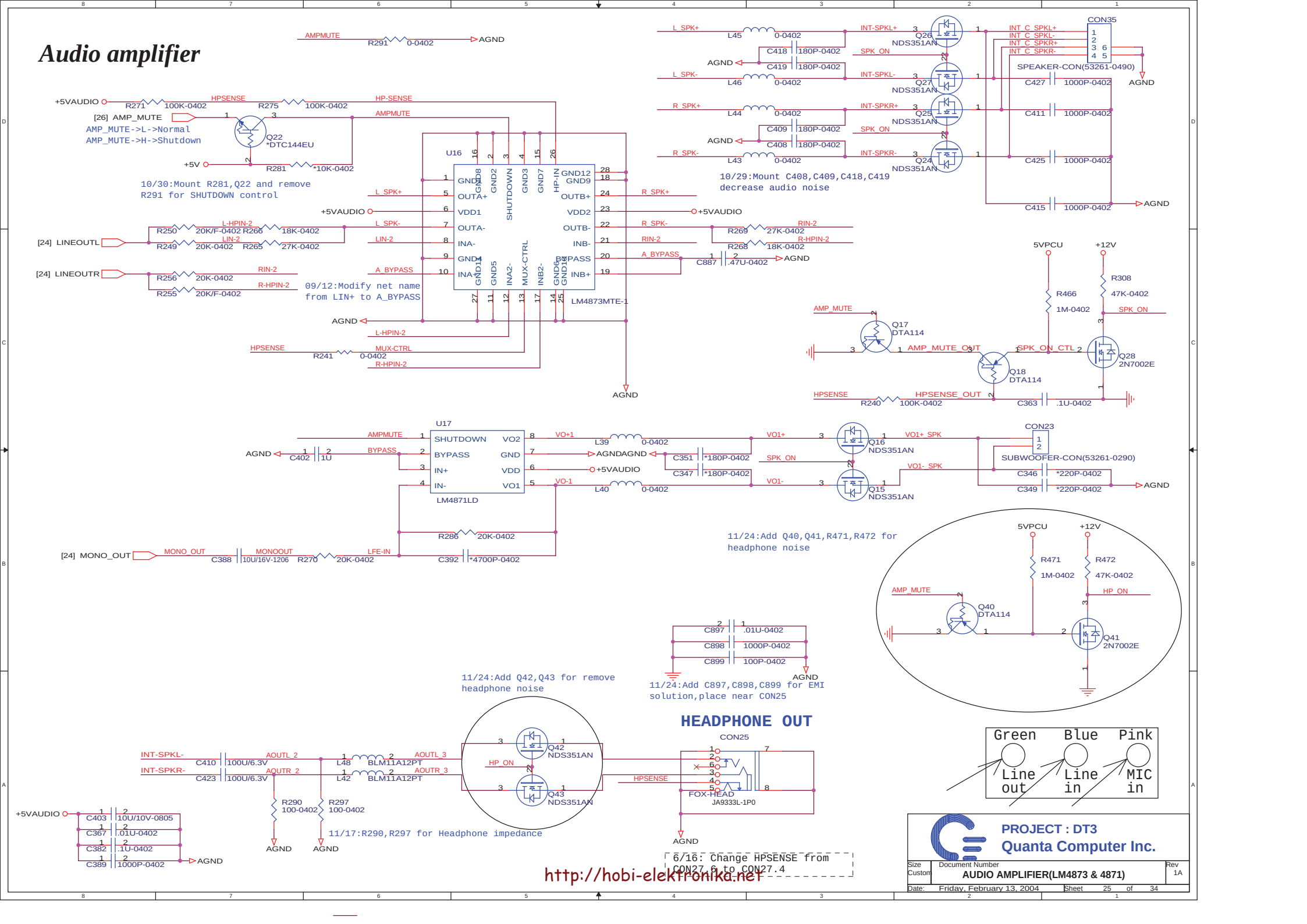

*HOLE-TC354BC433D118P2-8
HOLE1
HOLE2
HOLE9
HOLE11
HOLE17
HOLE19
*HOLE-C354D118P2-8
HOLE8
*Hole-TC236BC433D118P2
HOLE12
HOLE15
*Hole-TC236BC472D118P2
HOLE6
*H-C236D106P2
6/10: Change Hole5 footprint from 9mm to 6mm
6/10: Change HOLE3 footprint
CPU
HOLE5
Hole-C236D157P2
HOLE4
Hole-C236D157P2
HOLE7
H-C236D157P2
HOLE18
Hole-C236D157P2
*Hole-C236D118P2
HOLE10
HOLE13
Hole-C236D157P2
Hole-C236D157P2
HOLE16
HOLE14
Hole-C236D157P2
Hole-H-C315D185P2
HOLE3
SCREW HOLES
EMI FENSES
09/06:change P12 footprint to H-C197(from EMIPAD)
P21 P3 P23 P5 P10 P2 P7 P25 P1 P8 P39 P14 P20 P45 P17 P19 P47 P12 P16
P46 P26 P6 P13 P31 P9 P11
P36 P24 P44 P37 P35 P28 P22 P41 P43 P34
*ATE-C-018 *ATE-C-019 *ATE-C-020 ATE-C-021 ATE-C-022 *ATE-C-023 *ATE-C-024 *ATE-C-025 *ATE-C-026 *ATE-C-027
P42 P38 P4 P30 P27 P40 P32
*ATE-C-015 *ATE-C-015 *ATE-C-017 *ATE-C-015 *ATE-C-017 ATE-C-028 *ATE-C-029
6/12: Delete P23 for PE
GND_LAN_CHASIS
09/03:change from GND_LAN to GND_LAN_CHASIS
Serial Port
6/10: Change CPU nut footprint & total with 4 holes
7/16: Delete HOLE7 for DDR routing
U1
MAX3243
-RTS1 -DTR1 TXD1 -DSR1 RXD1A -CTS1 -DCD1 -RI1
T1I T2I T3I RIO R2O R3O R4O R5O
T1O T2O T3O R1I R2I R3I R4I R5I
BRTS1# BDTR1# BTXD1 BDSR1# BRXD1 BCTS1# BDCD1# S_BRI1
FORCEON FORCEOFF# INVAILD# R2OUTB
FORCE_ON INVAILD# R2OUTB
S_C1+ S_C1- S_C2+ S_C2-
VCC V+ V- GND
S_V+ S_V-
R317 10K-0402
C463 .47U-0402
C29 .47U/16V
C465 .47U/16V
C464 1U/25V
C460 .47U/16V
D31 RB500
BRI1
RI# [10,18]
R18 1K-0402
*TP TP3
*TP TP2
P33
*ATE-C-015
7/16: Remove P45, for shortage issue
BTXD1 BCTS1# BDTR1# RI# BDCD1# BDSR1# BRXD1 BRTS1#
RN6 0-8P4R
RN5 0-8P4R
BTXD-1 BCTS-1# BDTR-1# BRI-1 BDCD-1# BDSR-1# BRXD-1 BRTS-1#
BRI-1 BDTR-1# BCTS-1# BTXD-1 BRTS-1# BRXD-1 BDSR-1# BDCD-1#
C446 100P-0402
C444 100P-0402
C4 100P-0402
C448 100P-0402
C447 100P-0402
C445 100P-0402
C5 100P-0402
C449 100P-0402
CON3
COM(FOX-1019-W31)
RTC CIRCUIT
12/27:Place near ICH5
12/27:Change R30 to 180K for RTCRST timing require
12/29:Change 3V591 to 3VSUS
VCCRTC
3VSUS
D5 RB500
C50 1U
BAT
D7
R31 1K-0402
RB500
RTC_RST# Delay 18-25ms
R30 180K
RTCRST#
RTCRST# [10]
G1
SHORT_ PAD1
C40 .1U-0402
5VPCU
RTC_N01
Q44 MBT3904
R475 3K/F
R476 4.7K
R477 15K
CON27
Lithium-CON
RTC Backup battery
12/27:delete SW3,C44,C46,C53,C55,R29,D6
add Q44,R475,R476,R477
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
Size Custom
Document Number
EMI PAD & COM1 & RTC
Rev 1A
Date: Friday, February 13, 2004
Sheet 22 of 34

PC87391, FIR


PC87393
U2
PC87391
LPT Interface serial resistor
RN9 22X4
RN8 22X4
RN7 22X4
RN10 22X4
0822:X-Bus Reset Configuration Mode.
Pins
2 1 0 Functionality
x 0 0 No BIOS1
x 0 1 Normal Mode, XRDY disabled
0 1 0 Latch Mode, XA12-19, XRDY enabled
1 1 0 Latch Mode, GPIO10-17, XRDY enabled
0 1 1 Latch Mode, XA12-19, XRDY disabled
1 1 1 Latch Mode, GPIO10-17, XRDY disabled
08/22:BADDR(pin61)Base Address
No pull-up resistor: 2Eh-2Fh
10K external pull-up resistor: 4Eh-4Fh
Trace 60 mil or six via to +5V power plane
60 MIL
40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD1, RXD1
PROJECT : DT3
Quanta Computer Inc.
SIO PC87391, FIR, FDD
Friday, February 13, 2004
http://hobi-elektronika.net


ALC650 /ALC202 /ALC201 /ALC101
U19
40 MIL
+3V
+5VAUDIO
AUDIO_VDD
VDD1
VDD2
GND1
GND2
AVDD1
AVDD2
AGND1
AGND2
SYNC
SDIN
SDOUT
RESET#
BITCLK
VRAD
VRDA
GPIO0
XTLSEL
LINE-OUT-L
LINE-OUT-R
MONO-OUT
SURR-OUT-L
SURR-OUT-R
VREFOUT
VREF
NC1
NC2
Front-MIC
CENTER-OUT
LFE-OUT
EAPD / SPDIFI
SPDIFO
AFILT1
AFILT2
AUX-L
AUX-R
VIDEO-L
VIDEO-R
CD-L
CD-R
CD-GND
LINE-L
LINE-R
PC-BEEP
MIC1
MIC2
PHONE
XTL-IN
XTL-OUT
ALC650/ALC202/ALC201/ALC101
CD_IN(1)
MIC_IN (PINK)
FOX-MIC
JA9333L-1P0
CON24
CON28
CON29
*2P-JUMPER
AGND
10/24:From CDROM
10/24:Add R468,R469,R470 for CD_IN noise(don't cross plane)
08/26:Add for CD_GND
08/26:Change from 2.2K -> 4.7K
12/22:Change C_VREFOUT from CON24 pin3 to MIC_IN-2
12/22:Cut CON24 pin 4,5,6 from AGND,prevent noise feedback
10/14:Change C387 from .1u-0402 to 10u-1206
Place these capacitors near Ampifier.
10/29:Change C413 to .1U-0402 for PHONE_IN noise(C414 no mount)
11/24:Add C894,C895,C896 for EMI solution,place near CON24
6/12: Delete C701, C818, R516 for phone in design
03/06 D32 STAFF
10/06:(1)Remove D41,R462(THERM_BEEP)
6/12: Delete R564, reserve R565
01/13:16.Remove C890,C891 for PC-BEEP issue
09/12:Must be place between AGND and GND
6/12: Change R354, R492 from 1K-0402 to 68K-0402
Audio Power
AME8815BEGT475
500mA
10/20:Change U15 pin 1 to Vout
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
AUDIO CODEC(ALC202)
Friday, February 13, 2004

Audio amplifier



EC (NS87591L)

01/13:D33(S/S) change from 1SS355 to 0-0805(CS00004JA07),fan can full speed
02/07:Remove D33 and connect 3V_591 with AVCC

10/16:Change P/N from AKE3CZ00A00 to AKE3L8S0A03

11/13:PC97551 and PC87591Can't be without AVcc
Mount D33,R413,remove Q37,C273,C284,Q9

11/17:Remove CON32(EC Debug port) for ESD issue

02/22:ENV1 doesn't need pull-up, 591L running at IRE mode

SHBM=1: Enable shared memory with host BIOS

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1	Reserved	

Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.

08/20:No use must be grounded

10/09:Add BT_PWRON# pin

11/19:Add D44 for VCORE_GD to HWPG

08/10:Add for HWPG

If pin24(LPCPD#) is not pull high,system will not able to boot

02/11:Change CON 12(C/S) from DG032000002 to AKE34ZAPK01.(Remove BIOS socket and direct mount BIOS)

Pin 103 internal is "A19",Can't use to GPIO

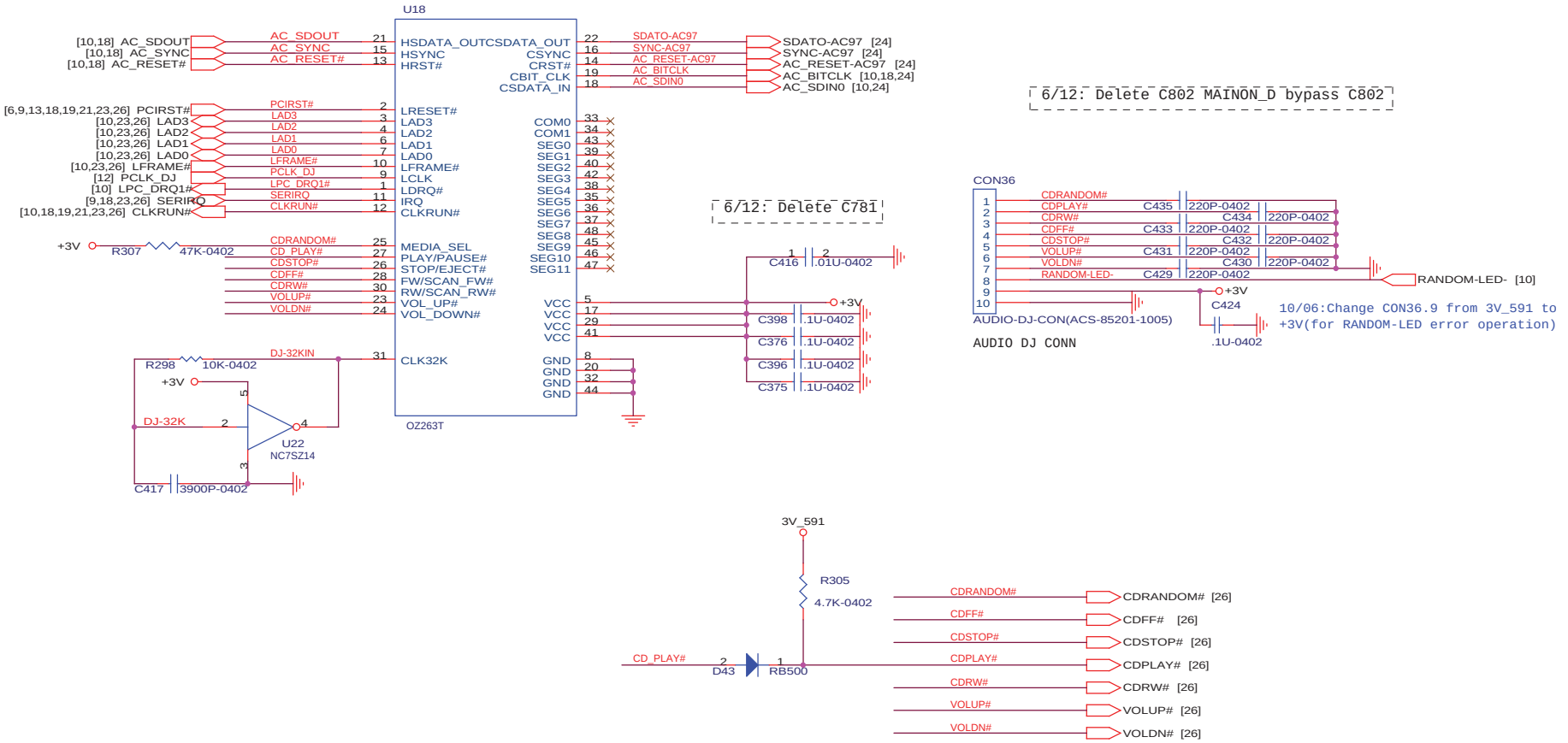
BIU configuration should match flash speed used

11/25:Add C900 for 87551L

1) -NBSWON
2) ACIN

PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	EC (NS87591L)	1A
Date: Friday, February 13, 2004	Sheet 26 of 34	



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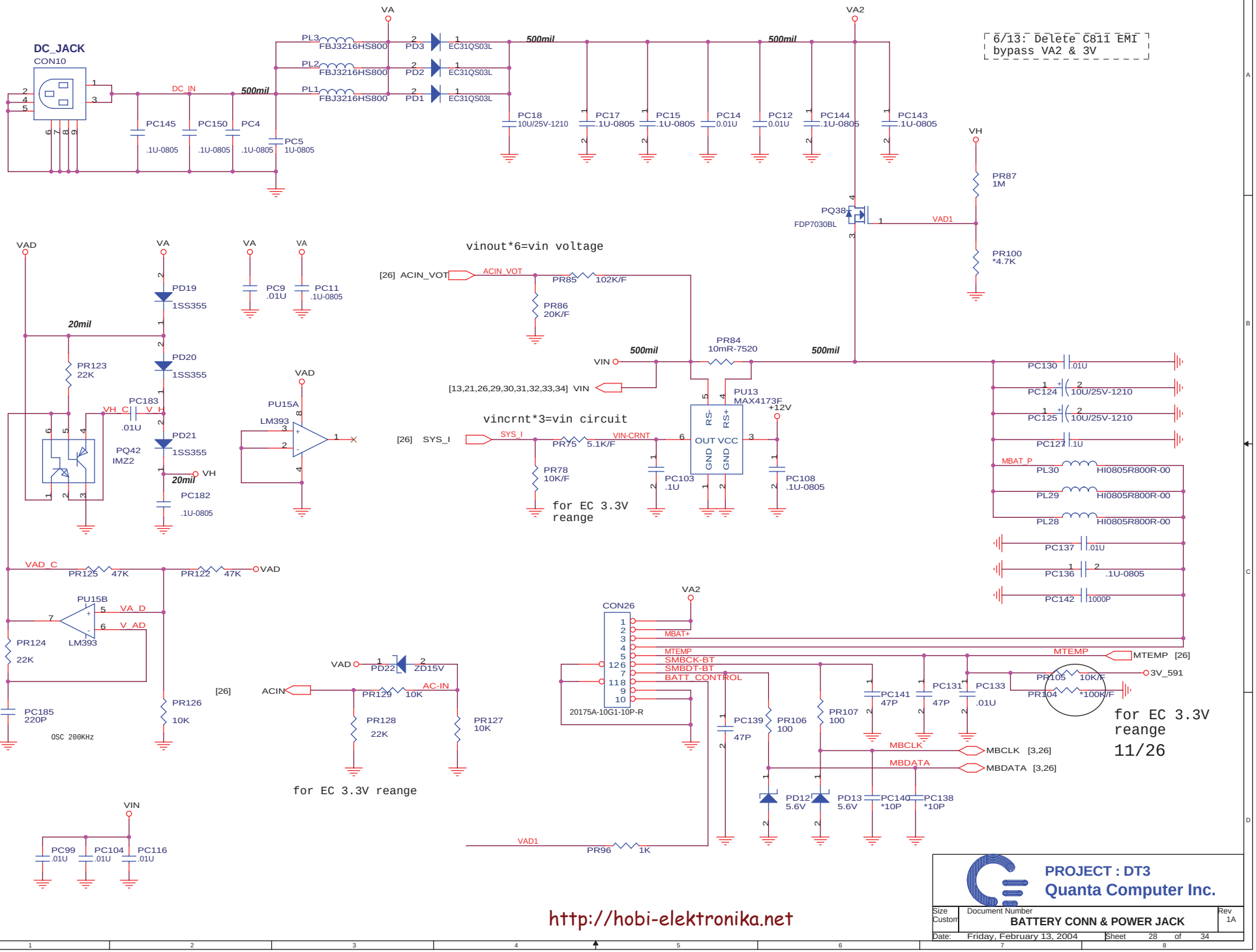
PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	OZ263 AUDIO-DJ	1A
Date: Friday, February 13, 2004	Sheet 27 of 34	



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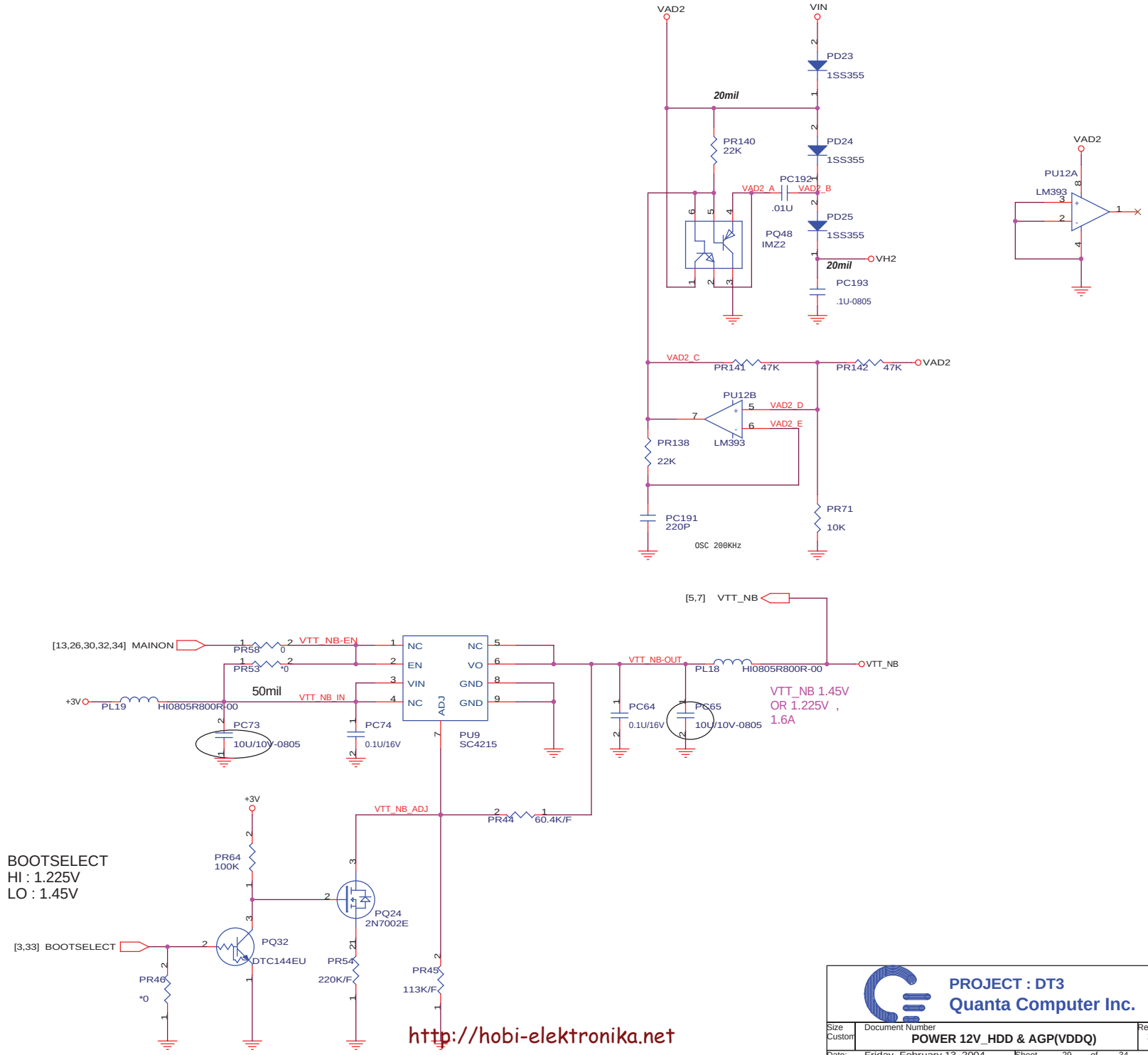
PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	BATTERY CONN & POWER JACK	1A
Date: Friday, February 13, 2004	Sheet 28 of 34	



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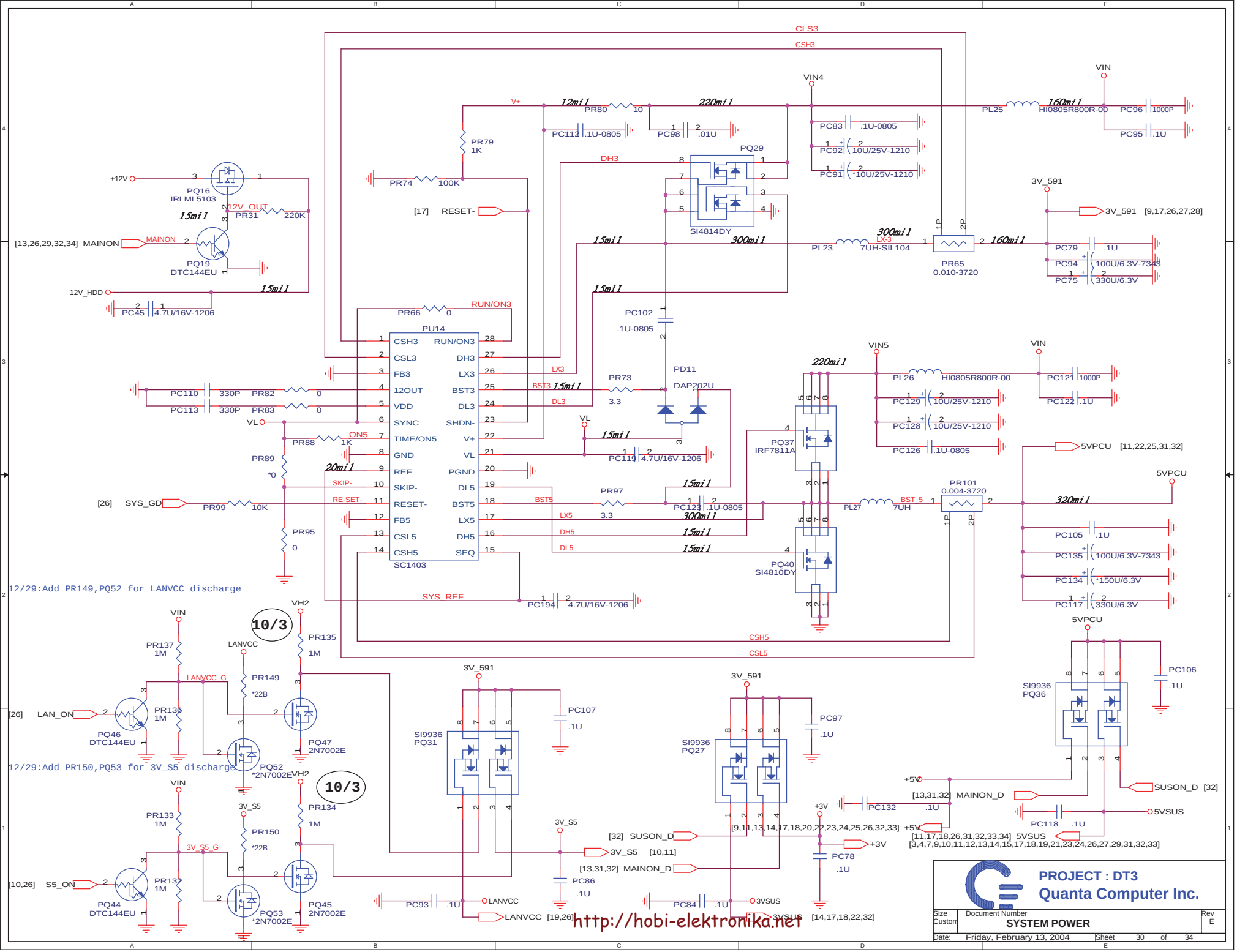
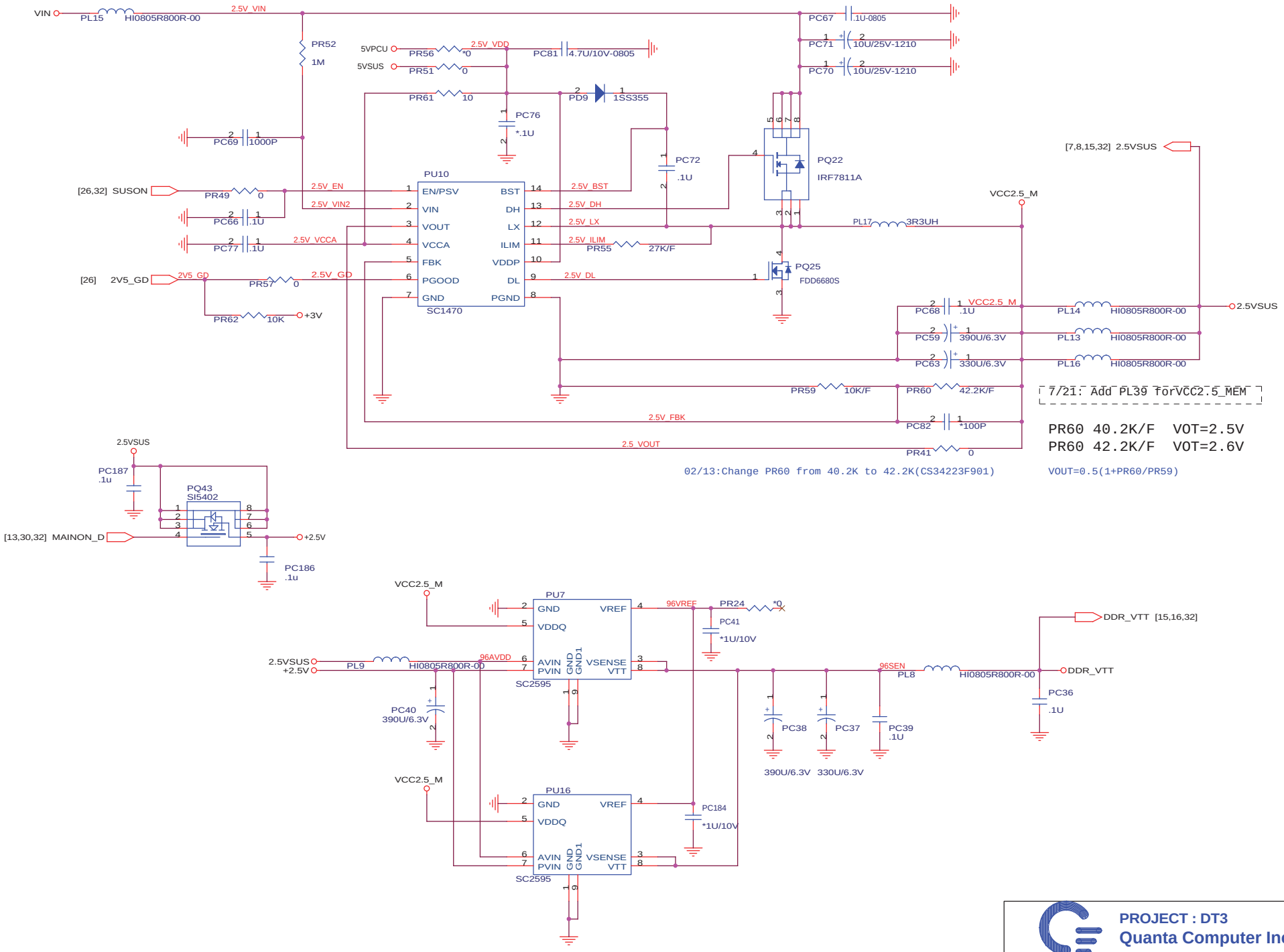
PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	POWER 12V_HDD & AGP(VDDQ)	1A
Date: Friday, February 13, 2004	Sheet 29 of 34	


CLS3
CSH3
VIN
VIN4
V+
12mil
220mil
160mil
PR80 10
PC112 .1U-0805
PC98 .01U
PL25 HI0805R800R-00
PC96 1000P
PC95 .1U
PC83 .1U-0805
PC92 10U/25V-1210
PC91 *10U/25V-1210
PR79 1K
PR74 100K
[17] RESET-
DH3
PQ29
SI4814DY
+12V
PQ16 IRLML5103
12V_OUT
PR31 220K
15mil
[13,26,29,32,34] MAINON
MAINON
PQ19 DTC144EU
12V_HDD
PC45 4.7U/16V-1206
300mil
PL23 7UH-SIL104
LX-3
1P
2P
PR65 0.010-3720
3V_591
3V_591 [9,17,26,27,28]
PC79 .1U
PC94 100U/6.3V-7343
PC75 330U/6.3V
PR66 0
RUN/ON3
PU14
CSH3
CSL3
FB3
12OUT
VDD
SYNC
TIME/ON5
GND
REF
SKIP-
RESET-
FB5
CSL5
CSH5
RUN/ON3
DH3
LX3
BST3
DL3
SHDN-
V+
VL
PGND
DL5
BST5
LX5
DH5
SEQ
SC1403
PC102 .1U-0805
PD11
DAP202U
PR73 3.3
PC110 330P
PR82 0
PC113 330P
PR83 0
PR88 1K
ON5
PR89 *0
PR95 0
[26] SYS_GD
PR99 10K
SKIP-
RE-SET-
20mil
PC119 4.7U/16V-1206
PR97 3.3
PC123 .1U-0805
PC194 4.7U/16V-1206
SYS_REF
VIN5
PL26 HI0805R800R-00
PC129 10U/25V-1210
PC128 10U/25V-1210
PC126 .1U-0805
PC121 1000P
PC122 .1U
PQ37 IRF7811A
PQ40 SI4810DY
PR101 0.004-3720
BST_5
PL27 7UH
5VPCU
5VPCU [11,22,25,31,32]
320mil
PC105 .1U
PC135 100U/6.3V-7343
PC134 *150U/6.3V
PC117 330U/6.3V
12/29:Add PR149,PQ52 for LANVCC discharge
12/29:Add PR150,PQ53 for 3V_S5 discharge
10/3
VH2
PR135 1M
PR137 1M
LANVCC
PR149 *22B
LANVCC_G
[26] LAN_ON
PR136 1M
PQ46 DTC144EU
PQ47 2N7002E
PQ52 *2N7002E
PR134 1M
PR133 1M
3V_S5
PR150 *22B
3V_S5_G
[10,26] S5_ON
PR132 1M
PQ44 DTC144EU
PQ45 2N7002E
PQ53 *2N7002E
SI9936 PQ31
PC107 .1U
PC93 .1U
LANVCC [19,26]
PC86 .1U
3V_S5 [10,11]
SI9936 PQ27
PC97 .1U
[32] SUSON_D
[13,31,32] MAINON_D
PC84 .1U
3VSUS
[14,17,18,22,32]
PC78 .1U
+3V
[9,11,13,14,17,18,20,22,23,24,25,26,32,33]
SI9936 PQ36
PC106 .1U
+5V
PC132 .1U
PC118 .1U
SUSON_D [32]
5VSUS
[11,17,18,26,31,32,33,34]
[3,4,7,9,10,11,12,13,14,15,17,18,19,21,23,24,26,27,29,31,32,33]
CSH5
CSL5
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
Size
Custom
Document Number
SYSTEM POWER
Rev
E
Date: Friday, February 13, 2004
Sheet 30 of 34


VIN
PL15 HI0805R800R-00
2.5V_VIN
PR52 1M
5VPCU
5VSUS
PR56 *0
PR51 0
2.5V_VDD
PC81 4.7U/10V-0805
PC67 .1U-0805
PC71 10U/25V-1210
PC70 10U/25V-1210
PR61 10
PD9 1SS355
PC76 *.1U
PC69 1000P
PC72 .1U
PQ22 IRF7811A
[7,8,15,32] 2.5VSUS
PU10
[26,32] SUSON
PR49 0
PC66 .1U
PC77 .1U
2.5V_EN
2.5V_VIN2
2.5V_VCCA
EN/PSV
VIN
VOUT
VCCA
FBK
PGOOD
GND
BST
DH
LX
ILIM
VDDP
DL
PGND
SC1470
2.5V_BST
2.5V_DH
2.5V_LX
2.5V_ILIM
PR55 27K/F
2.5V_DL
PQ25 FDD6680S
VCC2.5_M
PL17 3R3UH
[26] 2V5_GD
PR57 0
PR62 10K
+3V
PC68 .1U
PC59 390U/6.3V
PC63 330U/6.3V
PL14 HI0805R800R-00
PL13 HI0805R800R-00
PL16 HI0805R800R-00
2.5VSUS
PR59 10K/F
PR60 42.2K/F
7/21: Add PL39 forVCC2.5_MEM
2.5V_FBK
PC82 *100P
PR60 40.2K/F VOT=2.5V
PR60 42.2K/F VOT=2.6V
2.5_VOUT
PR41 0
02/13:Change PR60 from 40.2K to 42.2K(CS34223F901)
VOUT=0.5(1+PR60/PR59)
PC187 .1u
PQ43 SI5402
[13,30,32] MAINON_D
+2.5V
PC186 .1u
PU7
VCC2.5_M
2.5VSUS
+2.5V
PL9 HI0805R800R-00
96AVDD
96VREF
PR24 *0
PC41 *1U/10V
GND
VDDQ
VREF
AVIN
PVIN
GND1
VSENSE
VTT
SC2595
PC40 390U/6.3V
PC38 390U/6.3V
PC37 330U/6.3V
PC39 .1U
96SEN
PL8 HI0805R800R-00
DDR_VTT [15,16,32]
DDR_VTT
PC36 .1U
PU16
PC184 *1U/10V
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
Size Custom
Document Number
POWER - DDR & 2.5V
Rev 1A
Date: Friday, February 13, 2004
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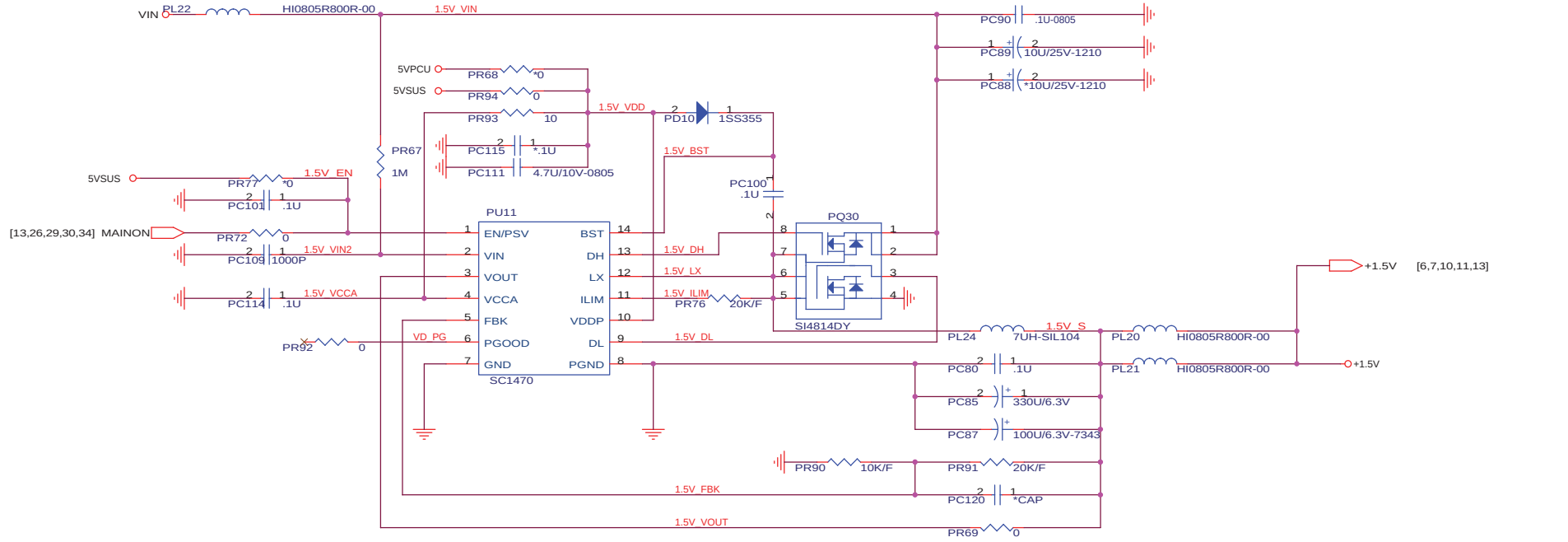

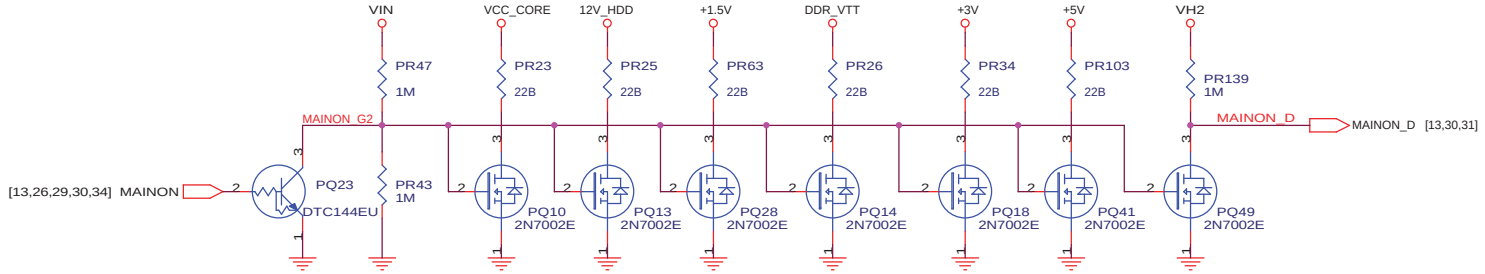

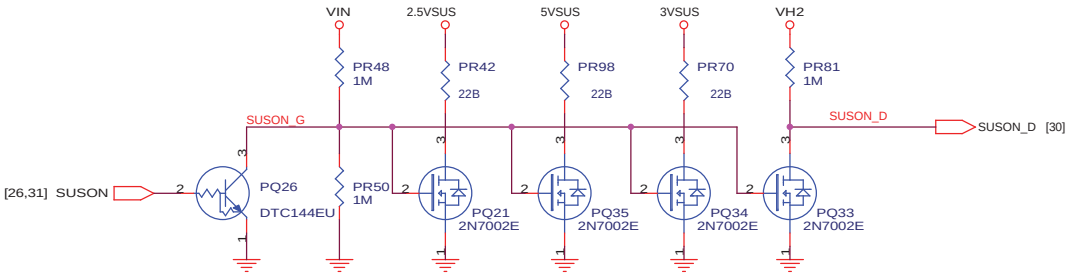

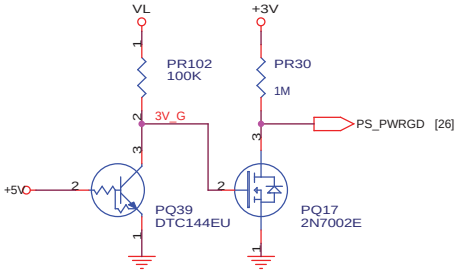

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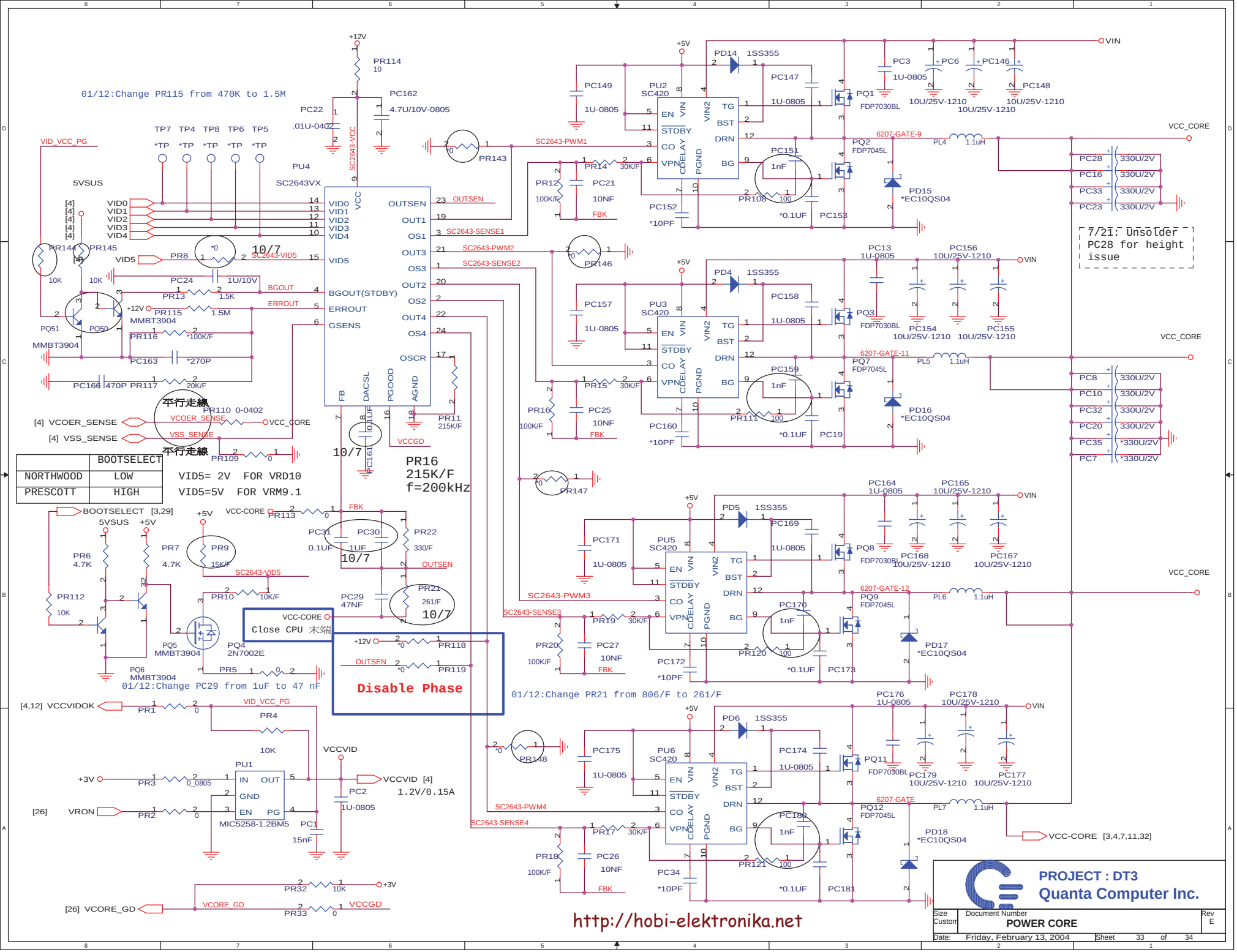
PROJECT : DT2
Quanta Computer Inc.

Size	Document Number	Rev
Custom	POWER DISCHARGE	E
Date: Friday, February 13, 2004	Sheet 32 of 34	


01/12:Change PR115 from 470K to 1.5M
7/21: Unsolder PC28 for height issue
PU4 SC2643VX
PU2 SC420
PU3 SC420
PU5 SC420
PU6 SC420
PU1 MIC5258-1.2BM5
VIN
VCC_CORE
+5V
+12V
5VSUS
VID_VCC_PG
SC2643-PWM1
SC2643-PWM2
SC2643-PWM3
SC2643-PWM4
SC2643-SENSE1
SC2643-SENSE2
SC2643-SENSE3
SC2643-SENSE4
6207-GATE-9
6207-GATE-11
6207-GATE-12
6207-GATE
PQ1 FDP7030BL
PQ2 FDP7045L
PQ3 FDP7030BL
PQ7 FDP7045L
PQ8 FDP7030BL
PQ9 FDP7045L
PQ11 FDP7030BL
PQ12 FDP7045L
PD14 1SS355
PD4 1SS355
PD5 1SS355
PD6 1SS355
PD15 *EC10QS04
PD16 *EC10QS04
PD17 *EC10QS04
PD18 *EC10QS04
PL4 1.1uH
PL5 1.1uH
PL6 1.1uH
PL7 1.1uH
PR16 215K/F f=200kHz
10/7
平行走線
BOOTSELECT [3,29]
NORTHWOOD LOW VID5= 2V FOR VRD10
PRESCOTT HIGH VID5=5V FOR VRM9.1
Close CPU 未端
Disable Phase
01/12:Change PC29 from 1uF to 47 nF
01/12:Change PR21 from 806/F to 261/F
[4,12] VCCVIDOK
[26] VRON
[26] VCORE_GD
[4] VCOER_SENSE
[4] VSS_SENSE
VCCVID [4] 1.2V/0.15A
VCC-CORE [3,4,7,11,32]
http://hobi-elektronika.net
PROJECT : DT3
Quanta Computer Inc.
Size Custom
Document Number POWER CORE
Rev E
Date: Friday, February 13, 2004
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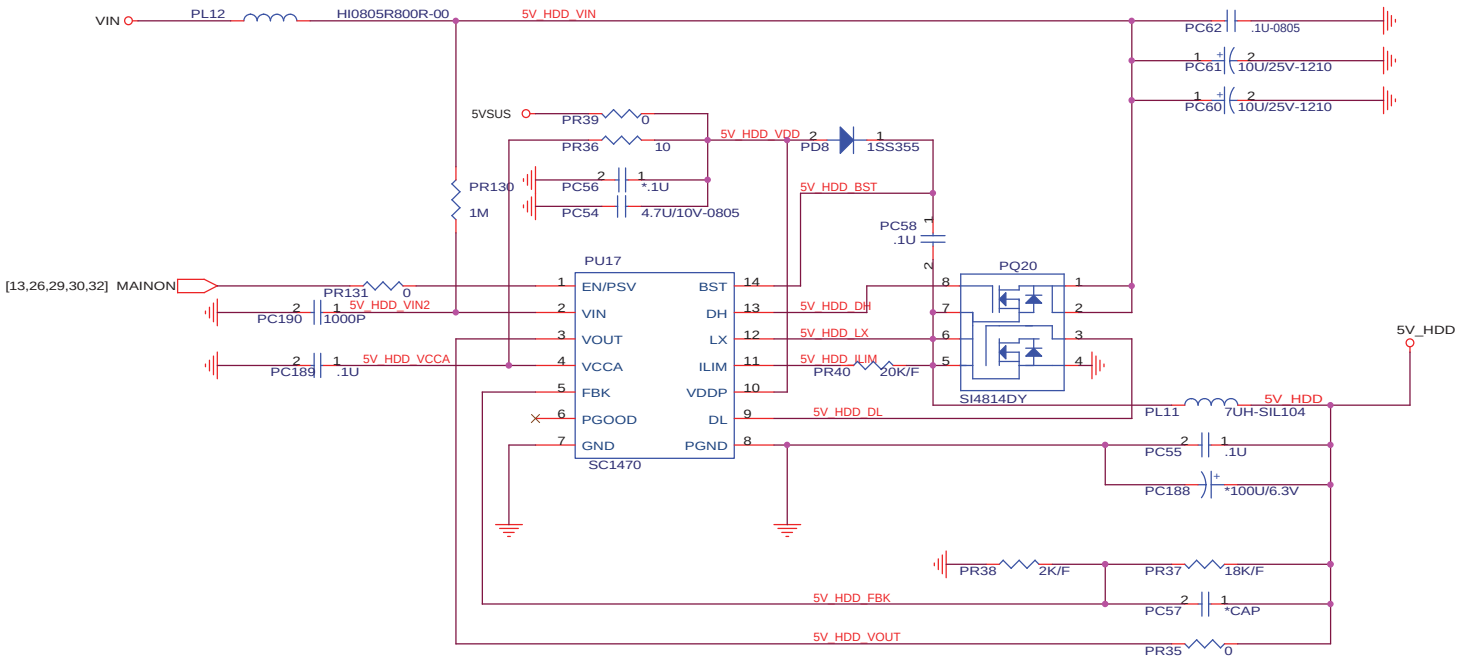

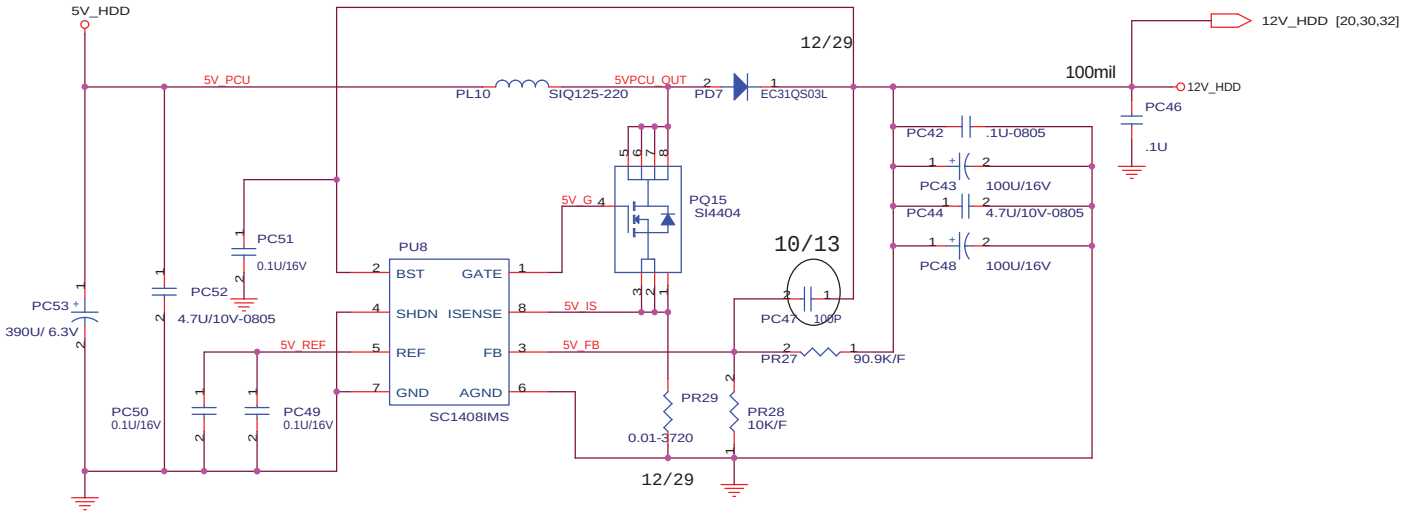

08/27 PC58 CHANGE COMPONENT TO 390U/6.3V

08/27 PR60 CHANGE FROM 10mohm TO 10mohm

PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	SYSTEM POWER 12V_HDD	1A
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